

Dr SATHISH KUMAR T M

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**Qualifications:**

S.No.	Degree / Diploma / Cert. Course	Specialization	University	College / School	Year of Passing	Percentage of Marks	Class / Division
1.	Ph.D	ICE/Low Power VLSI	Anna University, Chennai.	K.S.R.College of Engineering, Tiruchengode.	2019	-	-
2.	M.E	VLSI Design	Anna University, Chennai.	K.S.Rangasamy College of Technology, Tiruchengode.	2008	79 %	First Class with Distinction
3.	B.E	Electrical and Electronics Engineering	Anna University, Chennai.	Velalar College of Engineering and Technology, Erode.	2006	69 %	First Class
4.	DIPLOMA	Electrical and Electronics Engineering	Department of Technical Education	K.S.Rengasamy Institute of Technology, Tiruchengode.	2003	87.41 %	First Class with Honours
5.	S.S.L.C	-	-	Government High School, Sowdhapuram.	1999	62.2 %	-

Experience Details:

S.No.	Designation	Institution	Experience				
			From	To	Years	Months	Days
1	Lecturer	K.S.R. College of Engineering	21.01.2008	30.06.2010	02	05	09
2	Assistant Professor	K.S.R. College of Engineering	01.07.2010	10.01.2022	11	06	09
3	Associate Professor	K.S.R. College of Engineering	11.01.2022	Till date	2	06	10
TOTAL EXPERIENCE					16	5	28

Anna University Research Supervisor ID: 3840018**Area of Specialization:**

- VLSI Design, Digital Electronics and IoT

Publications:

a) International Journal: 20

1. S.Arulmurugan and T.M.Sathish Kumar, "VLSI Architecture Design for Analysis of Fast Locking ADPLL via Feed Forward Compensation Algorithm", "International Journal of Communication and Computer Technologies" July 2012, pp33-39. (ISSN 2278-9723)
2. R.Mahendran and T.M.Sathish kumar, "Low Power Adaptive Viterbi Decoder Design for Trellis Coded Modulation", "International Journal of Innovative Research in Electrical, Electronics, Instrumentation and Control Engineering" June 2013, pp123-128. (ISSN 2321 – 2004)
3. T.M. Sathish Kumar, Dr.P. S. Periasamy and C. Divya "A Low Power VLSI Architecture Design Using Enhanced ADPLL", "CiiT International Journal of Fuzzy Systems" May 2014(ISSN 0974 – 9608)
4. S.Keerthiga, S.SavithakarpagamandT.M.Sathish Kumar "Searching on Encrypted Cloud Documents using Keywords", "International Journal of Innovative Research in Computer and Communication Engineering" May 2015, pp4379-4384. (ISSN 2320-9801)
5. T.M. Sathish Kumar, Dr.P. S. Periasamy and G.Nandhini "All Digital Phase Locked Loop Architecture Design Using Vernier Delay Time-to-Digital Converter", "Australian Journal of Basic and Applied Sciences" June 2015, pp285-289. (ISSN 1991-8178)
6. G.Eswaran and T.M.Sathish Kumar "Analysis and Minimization of Low Power VLSI Design by Using Dynamic Phase Frequency Detector In ADPLL", "International Journal of Scientific & Engineering Research" February-2016, pp 60-64. (ISSN 2229-5518)
7. T.M.SathishKumar, P.S.Periyasamy and G.Eswaran"Analysis and Reduced the Complexity of ADPLL Architecture by Using of Digital Phase Frequency Detector", "Journal of Scientific Research in Computer Science & Information Technology" March 2016, Volume1, Issue 1 pp09-15
8. Sathishkumar.T.M and Dr.Periyasamy.P.S"Analysis and Design of Low Power All Digital Phase Locked Loop via Dynamic Logic Phase Frequency Detector in a Standard 0.25- μ m CMOS Technology", "Asian Journal of Information Technology" June 2016,ISSN 1682-3915, pp 1370-1381.
9. Sathish Kumar T.M, Dr.Periyasamy P.S and Eswaran.G"Analysis and Reduction of Power in All Digital PLLArchitecture by using Dynamic PFD with CWSP", "Asian Journal of Research in Social Sciences and Humanities" June 2016,ISSN 2249-7315, PP 2226-2242.
10. Sathish Kumar T.M, Dr.Periyasamy P.S "Energy Efficient All-Digital Phase Locked Loop Architecture Design on High Resolution Fast Clocking Time to Digital Converter (TDC) using Model Prescient Control (MPC) Technique" Wireless Personal Communications, Jan 2018, ISSN 0929-6212, Volume & Issue 102 & 4, pp 3343–3359.
11. Henna Sam.E.J, Pavithra.U.H, Ramakrishnan.P.V, RiyashBasha.S and Mr.T.M.Sathish Kumar "Underground Cable Fault Detection Using IOT", "Asian Journal of Applied Science and Technology (AJAST)" (Open Access Quarterly International Journal) Volume 2, Issue 2, Pages 737-746, April-June 2018, PP 737-746.
12. T.M.Sathish Kumar, R.Saraswathi and S.Ragavi "Design of Energy-Efficient IOT Devices using FINFET Based Secure Adiabatic Logic", "International Journal of Intellectual Advancements and Research in Engineering Computations", Volume-7 Issue-1, April 2019, PP 1511-1519.

13. Sathish Kumar, Oleg R. Kuzichkin, Ahmed Faisal Siddiqi, Inna Pustokhina&Aleks “Reliability Assessment of Ball Grid Array Joints Under Combined Application of Thermal and Power Cycling: Solder Geometry Effect”, “Soldering & Surface Mount Technology”, Volume 33 & Issue 1, May 2020 , PP 27-33.
14. AttuluriRakada Vijay Babu , D K Dheer, Y R Tagore, Sathish Kumar T M, SadullaShaik an Gorantla Srinivasa Rao, “A Review on the Progress of Intermetallic Solid-State Hydrogen Storage Material for Fuel Cell Vehicles”, “European Chemical Bulletin”, Feb 2022, Volume-11 Issue 1, PP 17-29, Jan 2022 (ISSN 2063-5346).
15. Dr.Poornima , Dr.Sathish Kumar T M, Veeramani , Karuppanasamy K, Rameshkumar J, Jothimani M and Mahendran R, “Solar Powered Floating Water Trash Collector”, “International Journal of Innovative Research in Technology”, Jan 2022, Volume 8 Issue 8, PP 162-164 (ISSN: 2349-6002)
16. Alex kumar. J and Dr T M Sathish Kumar, “Automatic Traffic control for Ambulance and VIP Vehicle”, “ Journal of Emerging Technologies and Innovative Research (JETIR)”, Volume 9 Issue 6, June 2022, (ISSN-2349-5162), PP 635-638
17. Sathish Kumar T M, Magibalan R, Mohanprasath A, Ragul T and Selva Praveen S, “A real Time Bus Monitoring System using Mobile Location”, International Journal of New Innovations in Engineering and Technology, April 2023, Volume 22 Issue 1, ISSN:2319-6319.
18. T.M.Sathish Kumar and P.Sujitha, “Huffman Coding Algorithm Based Adaptive Noise Cancellation”, “International Research Journal of Engineering and Technology”, Volume 10, Issue 6, June 2023, ISSN: 2395-0072, PP 252-259.
19. Dr.T.M.Sathish Kumar, Jaichandru.S, Kamaleshkanna.P, Mathiyazhagan.D and Sathish Kumar.G, “Vehicle-to-Vehicle Data Communication Through Light Fidelity(Li-Fi) Technology”, International Journal of Creative Research Thoughts (IJCRT)”, Volume 12, Issue 4, April 2024, ISSN:2320-2882.
20. T. Suganya, P. Mangaiyarkarasi, G. Thirugnanam and T. M. Sathish Kumar, ” A Hybrid Approach with MPPT Controller for Weed Cutting Based on Solar Powered Lawnmower with Minimal Intervention of Human Involvement Adopting IoT Technology”, “Analog Integrated Circuits and Signal Processing”, <https://doi.org/10.1007/s10470-024-02263-2>. 23.03.2024.

b) Patent: 03

1. The Patent Office Journal No. 12/2020 Dated 20/03/2020 Page: 14727
Title of the invention: System for Honey Collecting from Beehive using Remote control
Application No.202011011332 A
Publication Date: 20/03/2020
Issue No. 12/2020
2. S.No:152398
Design No: 364440-001
Date: 19/05/2022
Date of Issue: 05/01/2024
Title: Solar Charging Station
3. The Patent Office Journal No. 16/2024 Dated 19/04/2024
Application No.202441029331 A
Date of filing of Application: 11/04/2024
Publication Date: 19/04/2024

Title of the Invention: Vehicle-to-Vehicle Communication using Light Fidelity (LI-FI) Technology

c) Book:01

Title: IoT Fundamentals and Comprehensive Introduction
Authors: Ms. Sandhya Kumari, Dr. T M Sathish Kumar, Mrs. Hemalatha Saravanakumar, Dr.M.Baritha Begum and Mr. A K Chakravarthy
Publisher: COSMAS Scientific Publications
ISBN: 978-93-6096-504-4

d) Journal Reviewer:

- International Journal of Electronics Engineering
- Soldering & Surface Mount Technology

e) National Conference: 09

1. T.M.Sathish Kumar and R.Nanda Kumar, "FPGA Implementation of Non Zero Clock Skew Circuits", 5th National conference on Application of Emerging Technologies, Adhiyamaan College of Engineering, Hosur, March 2008, pp37.
2. T.M.Sathish Kumar, "Interconnect Pipelining in a throughput Intensive FPGA Architecture", National conference on Communication and Signal Processing, SSM College of Engineering March 2009.
3. T.M.Sathish Kumar, "Synthesis of Single Bit Control System Processor", National conference on Advanced Communication and Computing, Paavai College of Engineering, April 2009.
4. T.M.SathishKumar,E.D.Rajakumar and N,Manikandan"VLSI Architecture using Image Processing",National Conference on Emerging Trends in Electronics and Communication, Annai Vailankani College of Engineering, Feb 2011.
5. R.Mahendran and T.M.Sathish Kumar "Low Power Adaptive Viterbi Decoder Design for Trellis Coded Modulation", National Conference on Recent Trends in Communication, Computation and VLSI, Sri Ramanathan Engineering College, Feb 2013.
6. T.M.Sathish Kumar, "Aging Aware Reliable Multiplier Design with Adaptive Hold Logic", National Conference on Advanced Information and System for Sustainable Developments in Electrical and Electronics Engineering, Nandha College of Technology, March 2015.
7. T.M.Sathish Kumar "Under Ground Cable Fault Detection using IOT", 10th National Conference on Emerging Trends in Communication and Information Technology(ETCIT-18), K.S.Rangasamy College of Technology, March 2018.
8. T.M.Sathish Kumar "Alcohol Detection with Automatic Engine Locking System using GSM", Second National Conference on "Signal Processing and Communication Systems", M.Kumarasamy College of Engineering, March 2019.
9. T.M.Sathish Kumar "Protect Children from Child Abduction using real Time GSM Under Measureable Distance", Second National Conference on "Signal Processing and Communication Systems", M.Kumarasamy College of Engineering, March 2019.

International Conference: 08

1. S.Arulmuruganand T.M.SathishKumar, VLSI Architecture Design for Analysis of Fast Locking ADPLL via Feed Forward Compensation Algorithm” International conference on “Emerging Trends in Computer and Communication Technologies” Maria College of Engineering & Technology, Kanyakumari, Feb 2012, pp174-175.
2. T.M.SathishKumar Participated “3rdInternational Conference on Recent Trends in Information Technology”, Madras Institute of Technology, Chennai, July 2013.
3. T.M.SathishKumar“Analysis of Time to Digital Converter to Design a Low Power All Digital Phase Locked Loop”, “International Conference on Electrical, Electronics, Signals, Communication and Optimization, Kakinada, Jan 2015.
4. T.M. Sathish Kumar, Dr.P.S.Periasamy and , G.Nandhini“All Digital Phase Locked Loop Architecture Design Using Vernier Delay Time to Digital Converter”, “International Conference on Technological Convergence for Information, Health, Food and Energy Security”, Chennai, May 2015.
5. G.Eswaran and T.M.SathishKumar“Analysis and Minimization of Low Power VLSI Design by Using Dynamic Phase Frequency Detector In ADPLL”, “International Conference on Modern Engineering, Science & Technology– 2016 (IER-ICMEST’16)”, Trivandrum, Feb 2016.
6. G.Eswaran, T.M.Sathish Kumar and Dr.P.S.Periasamy“Analysis and Reduction of Power in All Digital PLL architecture by using Dynamic PFD with CSWP”, “2nd International Conference on Advanced Engineering and Technology for Sustainable Development” Karpagam College of Engineering, Coimbatore, Feb 2016.
7. T.M.Sathish Kumar “Design of Energy Efficient IOT Devices using FINFET Based Secure Adiabatic Logic”, International Conference on Recent Innovations in Engineering Technology and Management(ICRIETM-2019), Nandha Engineering College, March 2019.
8. T M Sathish Kumar “A Real Time Bus Monitoring System using Mobile Location”, 7th International Conference on Engineering, Technology and Science, Muthayammal College of bEngineering, Rasipuram, Namakkal, 31.03.2023.

Declaration:

I hereby declare that all the above information is true to the best of my knowledge.

Place : Veppadai
Date : 18.07.2024

Signature of the Applicant