K.S.R COLLEGE OF ENGINEERING
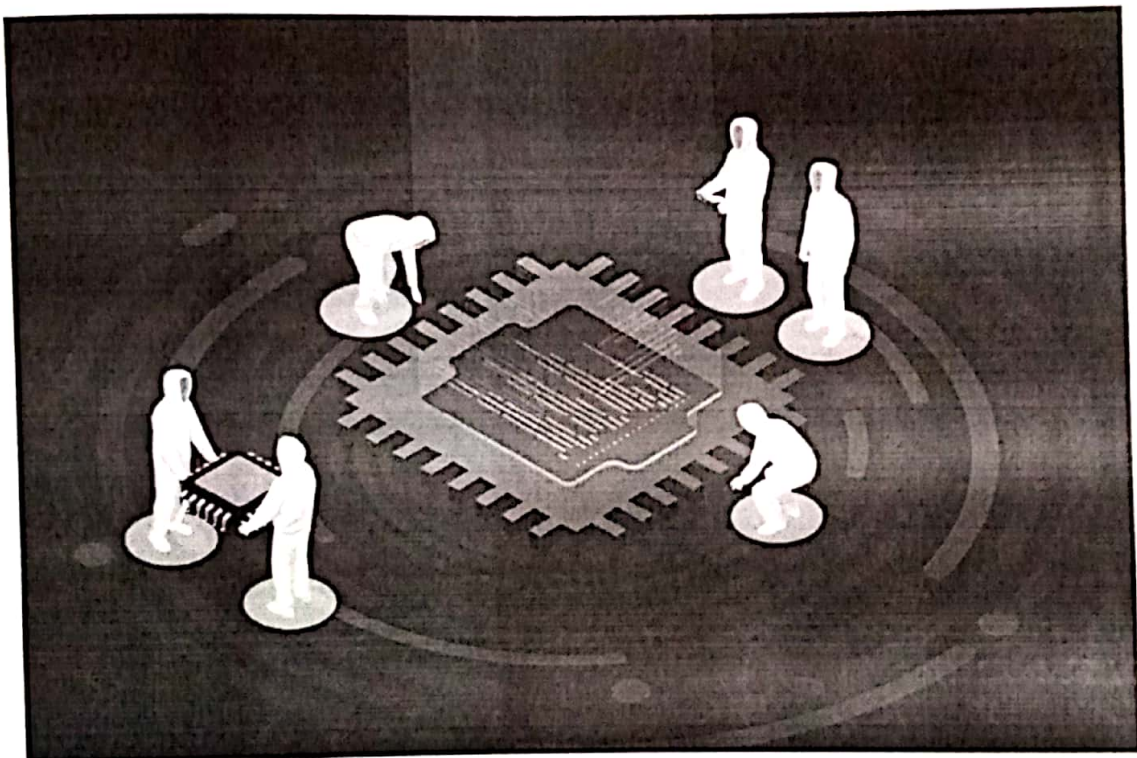
(AN AUTONOMOUS INSTITUTION)

NBA Accredited, Approved by AICTE and Affiliated to Anna University

TIRUCHENGODE-637215

Guest Lecture

On

VLSI DESIGN AND ITS APPLICATIONS

MR.V.GOWRISHANKAR, M.E, Ph.D

ASSOCIATE PROFESSOR/ECE,

VELALAR COLLEGE OF ENGINEERING AND TECHNOLOGY

Coordinator: P.Sivasankar Rajamani, Associate Professor/ECE

Date: 17.12.2021   Venue:MCA Seminar Hall

K.S.R.COLLEGE OF ENGINEERING,TIRUCHENGODE-637215

(AN AUTONOMOUS INSTITUTION)
NBA Accredited and ISO Certified Institution
Approved by AICTE and Affiliated to Anna University

WE CORDIALLY INVITE YOU ALL FOR THE GUEST LECTURE

On

"VLSI DESIGNS AND ITS APPLICATIONS"

Lion Dr. K.S.RANGASAMY.,MJF,

Founder and Chairman, K.S.R. Group of Institutions,
Tiruchengode.
Will kindly consented to deliver the Presidential Address

Thiru. R.SRINIVASAN,B.B.M,MISTE.,

Vice Chairman,K.S.R. Educational Institutions,
Tiruchengode.
Will kindly consented to deliver the Felicitation Address

Dr.P.SENTHILKUMAR,

Principal,
K.S.R.College of Engineering,Tiruchengode.
has kindly consented to preside over the function

Chief Guest

DR.V.GOWRI SHANKAR,

ASSOCIATE PROFESSOR, ECE DEPARTMENT,

Velalar College of Engineering and Technology,Thindal.

Coordinator :Mr.P.Sivasankar Rajamani, Associate Professor/ECE

Date:17.12.2021   Time:09.00 A.M

Venue :MCA Seminar Hall

ALL ARE WELCOME

AGENDA

Invocation	:	ECE Students
Welcome Address	:	Dr.P.S.Periasamy, Professor and Head, Department of ECE, K.S.R.College of Engineering,Tiruchengode.
Presidential Address	:	Lion Dr. K.S.RANGASAMY.,MJF, Founder and Chairman, K.S.R. Educational of Institutions, Tiruchengode.
Felicitation Address	:	Thiru. R.SRINIVASAN,B.B.M,MISTE., Vice Chairman, K.S.R. Educational Institutions, Tiruchengode.
Felicitation Address	:	Dr.P.SENTHILKUMAR, Principal, K.S.R.College of Engineering,Tiruchengode.
Inaugural Address	:	Dr.V.Gowri Shankar M.E,PH.D, Associate Professor of ECE, Velalar College of Engineering and Technology,Thindal.
Vote of Thanks		

K.S.R.COLLEGE OF ENGINEERING (AUTONOMOUS), TIRUCHENGODE -637215

(Autonomous)

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Date: 17.12.2021

Submitted to the principal

Sub: Requisition to conduct Guest Lecture-Reg.

We have planned to conduct Guest lecture on "VLSI Design and its Applications" for ECE students on 17.12.2021 from 9.00 a.m. to 2.00 p.m. .So we request you to grant the permission to conduct the Guest lecture on above mentioned date with a financial assistance of Rs.5000.

Resource person:

Dr.V.Gowri Shankar M.E,Ph.D,
Associate Professor of ECE,
Velalar College of Engineering and Technology,
Thindal.

IETE Coordinator

HOD/ECE

P.SivasankarRajamani,ASP/ECE

LETTER OF INVITATION

DATE: 10.12.2021

FROM

P.Sivasankar Rajamani,
Associate Professor/ECE,
K S R College of Engineering,
Tiruchengode.

TO

Dr.V.Gowri Shankar M.E,Ph.D,
Associate Professor of ECE,
Velalar College of Engineering and Technology,
Thindal.

Dear Sir,

On behalf of Department of Electronics and Communication Engineering - K S R College of Engineering, I am very pleased to have the honor of inviting you to deliver Guest Lecture at our college on "Vlsi design and its applications".

We have arranged the lecture on decemmber10, 2021 at 09.00a m. We are expecting an audience of approximately 71 students from our department.

It would be a great honor and privilege if you would graciously participate in our event and shared your expertise on the topic with our participants. Please let us know if you will require any audio-visual equipment or additional technological support. Also, please keep us informed of your travel itinerary.

We are anticipating your reply and look forward to spending the day with you and listening to your knowledgeable speech.

We look forward to seeing you soon.

CO-ORDINATOR
(P.Sivasankar Rajamani,)

HOD-ECE

PRINCIPAL

M Gmail

Sivasankar Rajamani <ssrajamani.vlsi@gmail.com>

Guest lecture-reg

3 messages

Sivasankar Rajamani <ssrajamani.vlsi@gmail.com>
To: shankargow@gmail.com

Tue,Dec 14,2021 at 4:37 PM

Respected sir,

we wish to arrange a guest lecture on " VLSI Designs and its applications" in our college for III year students .so kindly let me know your availability and the date which is convenient for you along with your profile.

Thanks and Regards,
T Sivashankar Rajamani,
Associate Professor,
Department of ECE,
K S R college of Engineering,Namakal.

📄 brochure.pdf
149K

v.gowrishankar <shankargow@gmail.com>
To: Sivasankar Rajamani <ssrajamani.vlsi@gmail.com>

Wed,Dec 15,2021 at 10:05 AM

Thank u I accept your invitation
[Quoted text hidden]

Sivasankar Rajamani <ssrajamani.vlsi@gmail.com>
To: "v.gowrishankar" <shankargow@gmail.com>

Wed,Dec 15,2021 at 10:06 AM

Thank you for your response.
[Quoted text hidden]

PROFILE

Guest Speaker

Name: Dr.V.Gowrishankar.

Department: Electronics and Communication Engineering.

Designation: Associate Professor.

Qualification:B.E.,M.E,Ph.D.

Velalar College of Engineering and Technology,Thindal.

DOJ: 10.06.2010

Email id: shankargow@gmail.com

ABOUT DR. V.GOWRISHANKAR

Mr V.Gowri Shankar obtained his B.E. (ECE) from Karpagam College of Engineering in the year 2001 and M.E. in VLSI Design from Anna University, Coimbatore in the year 2009 and Ph.D from Anna University,Chennai in November 2016. He has an industrial experience of around 4 years in R&D and more than 7 years of teaching experience. He is presently working as an Associate Professor in the Department of Electronics and Communication Engineering at velalar college of Engineering and Technology with a specialization in VLSI designs since 2010. He currently teaches both Under Graduate and Post Graduate Students.He has around 20 International Journal Publications, 10 National conferences and 8 International Conferences.

His research interests are in VLSI Design, Analog & Digital Circuits System design and Embedded Systems. He has previously been associated with Premier Evolvics Pvt. Ltd. as Team member, Firmware R & D, for 4 years.

He was into the design and development of real time embedded systems for textile related products. Her curiosity in design has earned the Best Performance Award in Research and Development of Projects in the year

He also has around 2.5 years of teaching experience as Lecturer in Nandha Engineering College where he taught subjects like Electronic devices and Circuits, Electronic Circuits I, II, Microprocessors and applications, VLSI Design, Fundamentals of computer and C programming.

He currently teaches both UG & PG level handling subjects like VLSI Design, Network Theory, Electronic devices and Circuits, C programming Theory &amp. Practice, Networks and Transmission lines, Automotive Electronics, Advanced Digital System Design, System Design using FPGA, CMOS VLSI Design, Low power CMOS circuits and memories, Measurements and Instrumentation.

He is a life member in CSI, ISTE, and member in IEEE in Engineering and also a NSS club coordinator and organized road safety Programmes.

K.S.R COLLEGE OF ENGINEERING

(AUTONOMOUS) TIRUCHENGODE

GUEST LECTURE
ON
"VLSI AND ITS APPLICATIONS"

By
Mr.V GOWRISHANKAR M.E., Ph.D
Associate Professor
Department of ECE
Velalar College of Engineering and Technology, Thindal

Vlsi companies in india
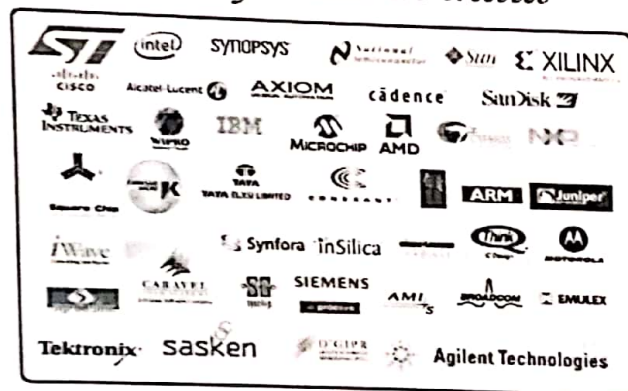
Area of work

Work on
- Memories
- PCB Design
- Digital security lock
- Robots
- Image processing
- MATLAB
- ARM KIT
- AVR KIT

products

Fature project
- FPGA Board design
- CPLD Board design
- DSP KIT Design
- microcontroller
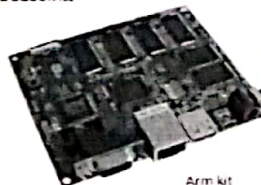
Arm kit
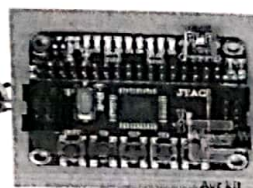
Avr kit

Application of VLSI
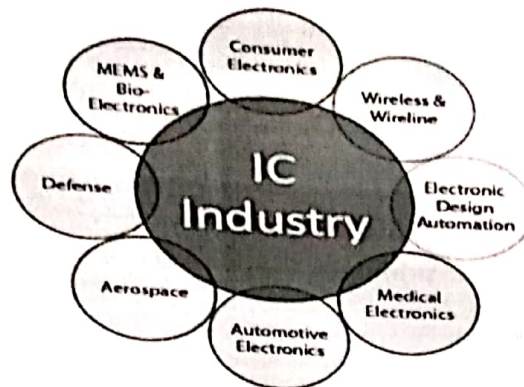
1. Ideas

Any thing we needs chip

- Microprocessor
- Microcontroller
- Memories
- Printer
- Mobile
- Digital security lock

Vlsi design flow

| 1. Idea (need) | 2. specifications | 3. design architecture | 4. RTL coding |

| 8.IC Chip | 7.Foundry | 6. Synthesis | 5. RTL Verification |
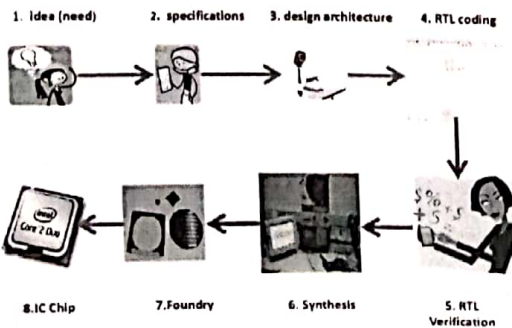
DATE: 17.12.2021

LETTER OF APPRECIATION

This is to appreciate and thank **Dr.V.GowriShankar,M.E.,Ph.D.,**through his session in Guest Lecture on "VLSI designs and its applications", conducted by our Department of Electronics and Communication Engineering on (17.12.2021)(Friday).This lecture helped the students to understand the concepts of Basics of Transistors, Need for MOS transistors, FPGA, SoC Architectures, Generation of ICs, Scope and Future of VLSI were discussed. The target audience for the session was III year ECE students. The overall feedback of the session was very good.

PRINCIPAL

K.S.R.COLLEGE OF ENGINEERING(AUTONOMOUS), TIRUCHENGODE -637215

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Impact Analysis on Guest Lecture

Department of Electronics and Communication Engineering had organized a Guest Lecture on "VLSI Designs and its Applications" on December 17th 2021 from 9.00 AM to 1:00 PM in MCA Seminar Hall.

The guest lecture began with welcome address by Dr.P.S.Periasamy,Head of the Department,ECE. He introduced and welcomed **Mr.V.GowriShankar,M.E.,Ph.D,Associate Professor,Velalar College of Engineering and Technology,Thindal**, resource person to deliver the lecture. The lecture was attended by 71 students and 5 Faculty members of Electronics and Communication Engineering Department.

The resource person took over the session at 10:05 AM discussing overview, Introduction to ICs, recent trends and applications of ICs, Small signal model and design of Analog ICs for future developments. He started the lecture from invention of the ICs, introduced how Moore's law satisfied the present scenario of IC fabrications.

He discussed briefly about advancements in microelectronics towards Analog IC design and its modeling. He briefly discussed the different examples for analog integrated circuit designs such as power management circuits, operational amplifiers, and sensors that are used with continuous signals for performing the functions such as active filtering, power distributing for components with in chip, mixing, and so on. He focused on circuit design and the techniques used to minimize capacitive and inductive noise.

He also introduced how to integrate analog circuits on large digital chips presents significant challenges, primarily due to substrate noise coupling. He differentiated a strategy for making analog circuits less sensitive to substrate bounce. He also summarized that CMOS processes move into the nanoscale—that is, less-than-100-nm—range, it becomes increasingly difficult to maintain the energy efficiency for medium- to high-accuracy analog circuits, Accuracies may decrease as the technology scales down.

Analog-chip designers can improve energy efficiency using different approaches, from selecting the optimum CMOS technology to clever system-level design. Finally he focused on data-converter performance has been steadily increasing over the years. Advancements in scaling and design techniques exploit the high density and speed of modern process technology. More intelligence is being put into data converters.

Managing more system functions, converters will simplify programming software, shrink, and become less complex and more costeffective with digital enhancement. Adaptive power management and sensor integration and enhancement are here, with more to come, eliminating the analog-circuit designer's messy task of signal conditioning the sensor and keeping the sensor, conditioning, and data-conversion chips as close as possible. He also discussed ADC converters employing pipeline architecture were the most appropriate for processing wideband signals with the required resolution.

He suggested the recent advances in oversampling-data-converter technology have enabled new alternatives for the definition of baseband analog front ends for wireless broadband communication systems. The technology for ADCs using oversampling sigma-delta architectures has evolved to the point that the devices can effectively convert signals with bandwidths of 10 MHz or more.

This achievement is significant because it allows for the use of this ADC architecture in the receiver channel of broadband communication interfaces, such as LTE (long-term evolution), WiMax, or 802.11abg. His current research interest is analog CMOS circuits for transceivers. He also suggested the future scope and research avenues availed in this domain.

Throughout this lecture, students gained a better understanding of trends in technologies and opportunities for future career growth. They also gained the knowledge of different techniques and models applied for developing new application or products.

K.S.R.COLLEGE OF ENGINEERING (AUTONOMOUS), TIRUCHENGODE -637215
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
Impact Analysis on Guest Lecture
From Participant's Feedback

S.No	Parameter to be Analyzed	Number of Students			
		Extremely Good	Good	Quite Good	Poor
1.	Overall Organization of the Lecture	64	6	1	-
2.	Relevant Information	50	10	11	-
3.	Time and Venue	23	34	10	3
4.	Kindling about the Subject Knowledge	43	12	10	6
5.	Anticipation of Speech	37	20	10	4
6.	Opinion about Speaker	38	20	10	3
7.	Usefulness of the session from the Knowledge and information point of view	66	4	1	-
8.	Overall effectiveness of the Lecture	45	20	5	1

- The Guest Lecture was very informative that the students learnt about fundamentals of " VLSI designs and applications". The lecture was even more interesting with the examples he gave relating to each topic. It was wonderful Knowledge sharing and gaining lecture regarding VLSI designs..

Impacts:

- This Guest lecture helped the students to improve their knowledge on the field of VLSI and its applications and the students may have the opportunity to participate in more workshops that have taken place under various topics on VLSI models, held at various other colleges.
- The Students may also be insisted to publish many articles on VLSI subject.
- The Guest Lecture has also motivated the students to do more projects on VLSI designs and Applications.

Impacts on Programme Outcomes:

PO1-Engineering Knowledge:
This Guest Lecture provides basic engineering knowledge on VLSI Designs and its Applications.

PO 3: Design a system,component,or process to meet desired needs within realistic constraints such as economic,environment,social,political,ethical,health,safety,manufacturability and sustainability

PO10: Communicate facts successfully with people in engineering domain and effectively design and prepare documents and reports.

PO11-Project Management and finance:
This lecture motivated many of the students to do project on VLSI Designs and its Applications.

K.S.R.COLLEGE OF ENGINEERING(AUTONOMOUS), TIRUCHENGODE -637215

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Guest Lecture on" VLSI DESIGNS AND ITS APPLICATIONS"

List of Participants

S.NO	Register Number	Name of the Student	sign
1	1915002	ARAVINTHAN T	
2	1915003	ARUNKUMAR S	T.Aravinth
3	1915004	ASHOKKUMAR K	S.A
4	1915005	ASWATH T	E.Ashokkum
5	1915006	DINESH S	T.Aswath
6	1915007	DINESHKUMAR T	S.din
7	1915008	DIWAHAR S J	T.D
8	1915009	GOWRIMANOHARI S	S.J.Di
9	1915010	GOWTHAM R	G.Gowri
10	1915011	ILAVARASAN K	R.Cut
11	1915012	INDUMATHI J	K.Meese
12	1915013	JAGADEEP S	J.Indu
13	1915014	JAWAHAR DHANUSH A	S.Jagdap
14	1915015	JAYAPRAKASH M	A.J.Dha
15	1915016	JEEVANANDHAM C P	M.Ja
16	1915017	KALIDAS M	C.Jeevanand
17	1915018	KAMARAJ J	Kalid.M
18	1915019	KARTHIKA DEVI K	Kamaraj
19	1915020	KAVIN M	KDK
20	1915021	KAVINESH K	M.Kavin
21	1915022	KAVINMUTHU S	K.Kavin
22	1915023	KIRAN C	kemuthu
23	1915024	KIRANRAJ T	K.C
24	1915025	KISHORE D	T.Kiran
25	1915026	KUMAR S	D.Kishi
26	1915027	LINGESH S	Skums
27	1915028	LOGESH S D	S.Lun
28	1915029	LOKENDRAN S	S.Dhogesh
29	1915030	MAGIBALAN R	S.lokent
30	1915031	MANOJ B	P.Kor
31	1915032	MANOJ T	B.Manoj
32	1915033	MOHAN S	T.Manl
33	1915034	MOHANASELAN D V	DV.Mohan

34	1915035	MOHANPRASATH A	
35	1915036	MONISHA D	
36	1915038	NAVEENA M	
37	1915039	PERUMAL R	
38	1915040	PRABAKARAN R	
39	1915041	PRADEEPA S	
40	1915043	PRITHIVRAJ T	
41	1915044	PRIYADHARSHINI V	
42	1915045	RAGUL T	
43	1915046	RATHI DEVI E	
44	1915047	REWINRAGUL R	
45	1915048	RUTHURESH K B	
46	1915049	SAKTHISREE M	
47	1915050	SANJAY R	
48	1915051	SANTHIYA G	
49	1915052	SELVA PRAVEEN S	
50	1915053	SETHU B	
51	1915054	SHALINI A	
52	1915055	SHARAN S	
53	1915056	SNEHA Y	
54	1915058	SOWMYASHRI M	
55	1915059	SREE GANESH M	
56	1915061	SUBASHINI E	
57	1915062	SUBHA N	
58	1915063	SUKUMAR V	
59	1915064	SUSHMA S	
60	1915065	SWATHI R	
61	1915066	TAMILARASU M	
62	1915068	THILAGA S	
63	1915069	THILAKBALAJE S M	
64	1915070	THIRUMURUGAN V	
65	1915071	VASANTH R	
66	1915072	VIGNESH V	
67	1915074	VINOTHINI G	
68	1915075	YOGESH R	
69	1915501	SABARI M	
70	1915502	SREE HARISH N	
71	1915503	TAMILSHRI S	

Faculty Co-ordinator

HoD-ECE

K.S.R. COLLEGE OF ENGINEERING,

K.S.R. Kalvi Nagar, **TIRUCHENGODE - 637 215.**

CASH/CHEQUE PAYMENT VOUCHER

Vr. No............................   Date....17.12.2021

Debit...

Pay to.....Dr. V. Gowrishankar...........a sum of Rs.....5000/-

Rupees........Five thousand only..............................

....

towards.....Renumeration for Guest Lecture on

"VLSI design and its applications"...........by Cash/Cheque/DD

No...Dated.........................

Recei........ve

Prepared by   **Cashier**   **Passed by**   **A.O.**   Payee's signature

K.S.R.COLLEGE OF ENGINEERING (AUTONOMOUS), TIRUCHENGODE -637215

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

FEEDBACK FORM FOR GUEST LECTURE

Register Number: 1915045.   **Name of the student:** Ragu.T

Topic: VLSI Design and its application.

Name of the Speaker: Dr. V. Gowrishankar.

If you attended the guest lecture we would like to make sure that the sessions were relevant and useful. Give your appropriate feedback to make the future lectures more beneficial for you.

Please '√' the option you find most suitable.

S.No	Particular	Extremely Good	Good	Quite Good	poor
1.	How was the overall organization of the lecture?		✓		
2.	How relevant was the content discussed by the speaker?	✓			
3.	Are you satisfied with the time and venue?		✓		
4.	How much interesting this session was for you?			✓	
5.	Did the lecture cover what you were expecting?			✓	
6.	What is your opinion about the speaker?		✓		
7.	How much this session was useful from the knowledge and information point of view?			✓	
8.	Overall effectiveness of the lecture?		✓		

Additional Comments and Suggestions for Future

I have learned about the informations.

Date: 30.5.22.   **Thank you**

K.S.R.COLLEGE OF ENGINEERING (AUTONOMOUS), TIRUCHENGODE -637215

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

FEEDBACK FORM FOR GUEST LECTURE

Register Number: 1915009   **Name of the student:** GOWRIMANOHARI S

Topic: VLSI design and its applications

Name of the Speaker: Dr. V. GOWRISHANKAR

If you attended the guest lecture we would like to make sure that the sessions were relevant and useful. Give your appropriate feedback to make the future lectures more beneficial for you.

Please '√' the option you find most suitable.

S.No	Particular	Extremely Good	Good	Quite Good	poor
1.	How was the overall organization of the lecture?	✓			
2.	How relevant was the content discussed by the speaker?		✓		
3.	Are you satisfied with the time and venue?		✓		
4.	How much interesting this session was for you?		✓		
5.	Did the lecture cover what you were expecting?			✓	
6.	What is your opinion about the speaker?		✓		
7.	How much this session was useful from the knowledge and information point of view?		✓		
8.	Overall effectiveness of the lecture?			✓	

Additional Comments and Suggestions for Future

_____

_____

Date: 17/12/2021   **Thank you**

K.S.R.COLLEGE OF ENGINEERING (AUTONOMOUS), TIRUCHENGODE -637215

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

FEEDBACK FORM FOR GUEST LECTURE

Register Number: 19EC019   **Name of the student:** K. KARTHIKA DEVI

Topic: VLSI design and its applications

Name of the Speaker: Dr. U. Gowrishankar

If you attended the guest lecture we would like to make sure that the sessions were relevant and useful. Give your appropriate feedback to make the future lectures more beneficial for you.

Please 'v' the option you find most suitable.

S.No	Particular	Extremely Good	Good	Quite Good	poor
1.	How was the overall organization of the lecture?	✓			
2.	How relevant was the content discussed by the speaker?		✓		
3.	Are you satisfied with the time and venue?			✓	
4.	How much interesting this session was for you?				✓
5.	Did the lecture cover what you were expecting?			✓	
6.	What is your opinion about the speaker?		✓		
7.	How much this session was useful from the knowledge and information point of view?	✓			
8.	Overall effectiveness of the lecture?			✓	

Additional Comments and Suggestions for Future

_____

_____

Thank you

Date: 17/12/2021

K.S.R.COLLEGE OF ENGINEERING (AUTONOMOUS), TIRUCHENGODE -637215

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

FEEDBACK FORM FOR GUEST LECTURE

Register Number: 1915041   Name of the student: S. PRADEEPA.

Topic: VLSI DESIGNS AND ITS APPLICATIONS.

Name of the Speaker: Dr. V. Gowrishankar

If you attended the guest lecture we would like to make sure that the sessions were relevant and useful. Give your appropriate feedback to make the future lectures more beneficial for you.

Please '√' the option you find most suitable.

S.No	Particular	Extremely Good	Good	Quite Good	poor
1.	How was the overall organization of the lecture?	✓			
2.	How relevant was the content discussed by the speaker?		✓		
3.	Are you satisfied with the time and venue?	✓			
4.	How much interesting this session was for you?		✓		
5.	Did the lecture cover what you were expecting?			✓	
6.	What is your opinion about the speaker?		✓		
7.	How much this session was useful from the knowledge and information point of view?	✓			
8.	Overall effectiveness of the lecture?			✓	

Additional Comments and Suggestions for Future

_____

_____

Date: 17/12/2021   Thank you

K.S.R.COLLEGE OF ENGINEERING (AUTONOMOUS), TIRUCHENGODE -637215

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

FEEDBACK FORM FOR GUEST LECTURE

Register Number: 19J5040   **Name of the student:** R. Prabakaran,

Topic: VLSI Designs And Its Application

Name of the Speaker: Dr. V. Gowrishankar.

If you attended the guest lecture we would like to make sure that the sessions were relevant and useful. Give your appropriate feedback to make the future lectures more beneficial for you.

Please '√' the option you find most suitable.

S.No	Particular	Extremely Good	Good	Quite Good	poor
1.	How was the overall organization of the lecture?	✓			
2.	How relevant was the content discussed by the speaker?		✓		
3.	Are you satisfied with the time and venue?	✓			
4.	How much interesting this session was for you?		✓		
5.	Did the lecture cover what you were expecting?		✓	✓	
6.	What is your opinion about the speaker?		✓		
7.	How much this session was useful from the knowledge and information point of view?	✓			
8.	Overall effectiveness of the lecture?				✓

Additional Comments and Suggestions for Future

_____

_____

Date: 17/12/2021   **Thank you**

K.S.R.COLLEGE OF ENGINEERING (AUTONOMOUS), TIRUCHENGODE -637215
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
Report on Guest Lecture

Topic: VLSI designs and its Applications

Speaker: Dr.V.Gowri Shankar M.E.,Ph.D

Faculty Co-Ordinator: P.Sivasankar Rajamani.

Date & Time: 17.12.2021 at 9.00 A.M.

Department of Electronics and Communication Engineering had organized a Guest Lecture on "VLSI Designs and Applications" on 17th December 2021 from 9.00 AM to 1:00 PM in MCA Seminar Hall.

The guest lecture began with welcome address by **Dr.P.S.Periasamy**, Head of the Department, ECE. He introduced and welcomed **Mr.V.GowriShankar, M.E., Ph.D, Associate Professor, Velalar College of Engineering and Technology, Thindal**, resource person to deliver the lecture. The lecture was attended by 71 students and 5 Faculty members of Electronics and Communication Engineering Department.

Guest Profile:

Mr V.GowriShankar ,obtained his B.E. (ECE) from Karpagam College of Engineering in the year 2001 and M.E. in VLSI Design from Anna University, Coimbatore in the year 2009 and Ph.D from Anna University, Chennai in November 2016. He has an industrial experience of around 4 years in R&D and more than 7 years of teaching experience. He is presently working as an Associate Professor in the Department of Electronics and Communication Engineering at velalar college of Engineering and Technology with a specialization in VLSI designs since 2010. He currently teaches both Under Graduate and Post Graduate Students..His research interests are in VLSI Design, Analog & Digital Circuits System design and Embedded Systems.

The session ended with a hearty thanks and presenting of a memento to Mr.V.GowriShankar by Dr.P.S.Periasamy in appreciation for taking the time out to educate the students on VLSI Designs and its Applications. He thanked the Management for providing the platform to organize guest lectures on innovative technologies. He appreciated Faculty members, student volunteers and participants for the successful conduction of the event.

As per the feedback received from the participants it was very good learning experience. This lecture helped the students to understand the concepts of Basics of Transistors, Need for MOS transistors, FPGA, SoC Architectures, Generation of ICs, Scope and Future of VLSI were discussed. The target audience for the session was III year ECE students. The overall feedback of the session was very good.