

K.S.R. COLLEGE OF ENGINEERING, TIRUCHENGODE – 637 215.

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING FACULTY BIO-DATA

01. Name : P. SIVASANKAR RAJAMANI
2. Designation : ASSOCIATE PROFESSOR
03. Date of Birth : 21.07.1977
04. Date of Joining : 03.12.2008



05. Qualification & Designation :

	At the time of Joining	Now
Qualification	M.E., MBA.,	M.E., MBA.,
Designation	Lecturer	Associate professor

06. Specialization : VLSI DESIGN

07. Area of Interest : VLSI DESIGN

08. Experience & Promotion Details (Chronological):

SL.NO.	Name of the Organization	Designation	Period	
			From	To
1	K.S.R College of Engineering	Associate professor	1.11.2015	till date
		Assistant Professor	01.07.2010	31.10.2015
		Lecturer	03.12.2008	30.06.2010

09. Publication Details: -

	National Level		International Level	
	Journal(s)	Conference(s)	Journal(s)	Conference(s)
KSRCE	-	9*	7*	7*
Others	-	-	-	-

*-Annexure

10. Details of Proposal

SL.NO	Title of the Proposal	Funding Agency Details	Date & Duration	Status
1	Autonomous Motion Controlled Hand – Arm Robotics Systems	Tamilnadu State Council for Science and Technology DOTE CAMPUS CHENNAI-600025	26.08.11	Rejected

11. Details of Winter/Summer Schools Attended

Sl.NO	Name of the programme	Organized by	Duration
1.	Universal Human Values -I	AICTE	05.12.2022 to 9.12.2022
2.	Mixed signal IC design – Emphasis on Data Converters	Maulana Azad National institute of technology, Bhopal	16.11.2015 to 20.11.2015
3.	Recent trends and advancements in VLSI for embedded applications	Chaitanya Bharathi institute of technology Hyderabad	15.06.2015 to 27.06.2015
4.	Staff Development Programme	Sona College of Engineering, Salem	16.11.09 to 28.11.09
5.	Instructional Design & Delivery	K.S.R. College of Engineering	17.08.09 to 22.08.09

12. Details of Workshops/ Seminars Attended

Sl.No	Name of the programme	Organized by	Duration
1.	IEEE Xplore Digital Library Training	KSR College of Engineering	4.07.2018
2.	Science academies lecture workshop on numerical methods and scientific computing	K.S. Rangasamy College of Arts and Science	10.08.2016 12.08.2016
3.	Faculty Empowerment Workshop on Electronic Circuits II & Laboratory	EQIP Foundation at KSR Groups of Institution	2.12.2014 6.12.2014
4.	EC2302- Digital Signal Processing	KSR College of Engineering	18.06.2013 to 24.06.2013
5.	EC 2252 communication theory	KSR College of Engineering	28.11.2012 5.12.2012
6.	MATLAB Applications In Engineering- Hands On Training	Sasurie College of Engineering, Vijaya Mangalam	15 th Mar 2012
7.	Workshop On Mobile Computing	Periyar University, Salem	30 th Aug 2008

13. Details of Winter/Summer Schools/Workshops/Seminars organized:

Sl.No	Name of the programme	Sponsored by	Acted as	Duration	Coordinator
1	Short Term Training Programme on Artificial Intelligence and Deep Learning using Python for Industrial Applications	AICTE	Co-Coordinator	15.02.2021 to 20.02.2021	Dr.P.S.Periasamy
2	Webinar on Exploring Cadence EDA Tool	KSRCE	Coordinator	19.12.2022	J,Rameshkumar

14. Details of the Subjects Handled (in KSRCE):

Sl.No	Academic Year	Semesters	Name of the Subject	Class	% of Pass
1	2022-2023	Odd	Electronic Devices	II ECE	Waiting for Result
		Even	Universal Human Value and Understanding Harmony	III ECE	Handling
2	2021-2022	Odd	Electronic Devices	II ECE	96.8 %
		Even	Electric circuits	I ECE	78.4 %
3	2020-2021	Odd	Electronic Devices	II ECE	92
		Even	Electric Circuits	I ECE	100
4	2019-2020	Odd	Professional Ethics	II ECE	92
		Even	Electric Circuits	I ECE	100
5	2018-2019	Odd	Professional Ethics	IV ECE	100
		Even	Electric Circuits	II ECE	87
6	2017-2018	Odd	Electronic Devices and Circuits	II ECE	52.8
			Television and video engineering	III ECE	65.9
		Even	Applications Specific Integrated Circuits	IV ECE	98.7
7	2016-2017	Odd	Electronic Devices and Circuits	II ECE	52.8
			Wireless cellular communication	IV ECE	82.5
		Even	Applications Specific Integrated Circuits	IV ECE	99.5
8	2015-2016	Odd	Electronic Devices and Circuits	II ECE	87.2
			Design of CPLD and FPGA	II M.E VLSI Design	100
		Even	Applications Specific Integrated Circuits	IV ECE	96.85
9	2014-2015	Odd	Electronic Devices and Circuits	II ECE	98.07
			DSP Architecture programming and its applications	II M.E VLSI Design	100
		Even	Electronic circuits	II ECE	91.22
10	2013-2014	Odd	Electronic Devices and Circuits	II ECE	88.06
			CMOS VLSI design	II M.E VLSI Design	100
		Even	Electronic circuits	II ECE	89.23
11	2012-2013	Odd	Electronic Circuits I	II ECE	44.92
		Even	Electronic Circuits II	II ECE	83.82
			Analysis and design of Analog Integrated Circuits	I M.E VLSI Design	100

Sl.No	Academic Year	Semesters	Name of the Subject	Class	% of Pass
12	2011-2012	Odd	Principles of Management	III ECE	95.23
			Electronic Circuits I	II ECE	98.5
		Even	Electrical Circuits & Electron Devices	I CSE	54
			Analysis and design of Analog Integrated Circuits	I M.E (VLSI Design)	94.44
13	2010-2011	Odd	Principles of Management	III ECE	95.7
		Even	Total Quality Management	IV-ECE	98.4
14	2009-2010	Odd	Microcontroller System Design and its Application	I M.E (EST)	100
		Even	Wireless Networks	IV-ECE	98.46
15	2008-2009	Even	Principles Of Management	III-ECE	98.43

15. Additional Responsibilities: -

SL.NO	Details of Work	Work allotted by	Duration
01	Association activities	Head of ECE	May 2022 to till date
02	Seminar/ workshop and grand	Principal	June 2018 to Dec 2021
03	Mentor	Head of ECE	June 2009 to Till Date
04	Furniture In charge	Head of ECE	June 2009 to Till Date
05	Class Committee Chair Person	Head of ECE	June 2010 to December 2010 Jan 2012 to May 2012 June 2012 to December 2012
06	Book Store co- In charge	Principal	June 2010 to June 2018
07	VLSI & Network lab In charge	Head of ECE	June 2013 to Till Date

16. Self-Appraisal

- 3 Major Strengths:
- 1 Interpersonal skill
 2. Adoptability
 3. Motivating others in positive sense
- 3 Major Weaknesses:
1. Short Tempered
 2. Frankness
 3. Moving Friendly with people

LIST OF PUBLICATIONS

National Conference: 09

1. **Sivasankar Rajamani.P**, (2019), 'Analysis of three IOT based wireless sensor for environment monitoring' in proceedings of the 6TH National Conference on Emerging trend in Engineering & Technology, Nandha college of technology Erode, Tamilnadu, pp 73.
2. **Sivasankar Rajamani.P**, (2019), 'Bioelometrik profi bus network architecture modifications 6TH National Conference on Emerging trend in Engineering & Technology, Nandha college of technology Erode, Tamilnadu, pp 96.
3. **Sivasankar Rajamani.P**, V.Kowsalya (March 2018), '**Garbage monitoring and cleaning using IOT**' in proceedings of the Fourth National conference on recent trends in electronic and communication at Jayalakshmi institute of technology thoppur (salem).
4. **Sivasankar Rajamani.P** (march 2017) "Driver drowsiness and alchol detection" national conference on computing perspective on smart city at kumaraguru college of technology Coimbatore
5. **Sivasankar Rajamani.P** (march 2017) "**Wireless gesture control for drainage cleaning using robot**" national conference on computing perspective on smart city at kumaraguru college of technology Coimbatore
6. **Sivasankar Rajamani.P** & R. Mariselvam (March 2015) "**Design of Low Power Multi- Bit Retention Register**" National conference on microwave and optical communication Alagappa Chettiar College of Engineering & Technology Karaikudi
7. **Sivasankar Rajamani. P** (April 2010) "**Mull volt Drop Armature Test RIG**" Two-day national conferences on automation, computing communication and control systems.
8. **Sivasankar Rajamani.P** & design of block XOR pre computation based address memory in preceeding of the first national conference on advanced communication and computing NAAC 09 Paavai College of engineering Rasipuram Tamilnadu PP-73
9. **Sivasankar Rajamani.P**, Prakash G (April **2009**) "VHDL Implementation OF block XOR pre computation based content addressable memory" in national conference on Emerging trends in communication and information technology held at K.S.Rangasamy College of technology, Tiruchengode.

International Conference: 07

1. Sivasankar Rajamani.P (May 2022) "Preserving of fruits and vegetables using Microcontroller based GSM" international conference at Arasu Engineering College.
2. Sivasankar Rajamani (May 2021) "charging an electric vechile using wind energy" 10th International conference on advance science and engineering research at Al-Ameen Engineering College, Erode.
3. Sivasankar Rajamani.P (March 2020) "Analyze the performance of Hybrid full adder using XOR and XNOR gates" 9th International conference on advance science and engineering research at Al-Ameen Engineering College, Erode.
4. Sivasankar Rajamani.p (March 2016) "Design and analysis of combinational circuits sing gate diffusion input technique" 2nd International conference on electrical electronics and computer engineering at Vivekananda College of Engineering for Women Tiruchenode.
5. Sivasankar Rajamani.P & M Gobinath (April 2014) "Low Power Content Addressable Memory Using NAND Cell" First international conference on intelligent engineering systems EASA College of Engineering & Technology.
6. Sivasankar Rajamani.P Niji johnson, (2012), 'Autonomous Motion Controlled Hand Arm Robotic Systems' in proceedings of the First international Conference on computational intelligence and its applications, ICCIA'12, Vivekanandha Institute of Engineering and Technology for Women Tiruchengode , Tamilnadu, pp 10.

7. Sivasankar Rajamani. P (April 2009) “VHDL Implementation of Block XOR Pre- Computation Based Content Addressable Memory” Emerging Trends in Communication and Information Technology KS Rangasamy College of Technology

National Journal: Nil

International Journal: 07

1. Sivasankar Rajamani P (2020) Analyse the performance of hybrid full adder using XOR and XNOR Gates” International of advanced science and engineering research [www.ijaser](http://www.ijaser.com) volume 5 issue 1 2020
2. Sivasankar Rajamani P & Usha.P FEB (2016) “**Design and Analysis of Combinational Circuits Using Gate Diffusion Input Technique** ON LINE JOURNAL
3. Sivasankar Rajamani.P & R. Mariselvam (March 2015) “**Design of Low Power Multi –Bit retention register**” **International Journal of Applied Engineering Research** special issue Volume 25.
4. Sivasankar Rajamani.P & Lincy (March 2014) “**Low Power SRAM Design Using Multi –Bit Flip-Flop**” **International Journal of Technology Enhancements and Emerging Engineering Research** ISSN2347-4289 Volume 2 Issue 4,
5. Sivasankar Rajamani.P & Lincy (March 2014) “**Clock Power Reduction Using Multi Bit Flip Flop**” ON **LINE JOURNAL** CIIT Programmable Devices Circuits and Systems. Volume 6 Issue 3.
6. Sivasankar Rajamani. P & R. Nithyalakshmi (March 2013) “**Power Efficient Counter Design Using Conditional Pulse Enhancement Flip-Flop**” **International Journal of Engineering Science and Innovation Technology (IJESIT)** Volume 2 Issue 2,
7. Sivasankar Rajamani. P & Niji Johnson (April 2012) “**A Hand –Arm Robotics System for Autonomous Motion Control**” **International Journal of Emerging Trends in Engineering and Development** ISSN: 2249-6149 **Volume3 Issue 2** **Volume 3, pp 52-59.**