

K.S.R. COLLEGE OF ENGINEERING, TIRUCHENGODE – 637 215.

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING FACULTY PROFILE

01. Name : P.THILAGAVATHI
02. Designation : ASSOCIATE PROFESSOR
03. Date of Birth : 28.02.1973
04. Date of Joining : 01.11.2007
05. Qualification & Designation :



	At the time of Joining	At Present
Qualification	M.E	M.E
Designation	LECTURER	ASSOCIATE PROFESSOR

06. Specialization : Applied Electronics
07. Area of Interest : VLSI Testing, Digital Electronics

08. Experience & Promotion Details (Chronological):

SL.NO.	Name of the Organization	Designation	Period	
			From	To
1	K.S.R. College of Engineering	Associate Professor	01.07.2010	Till Date
2	K.S.R. College of Engineering	Assistant Professor	01.07.2009	30.06.2010
3	K.S.R. College of Engineering	Lecturer	01.11.2007	30.06.2009
4	K.S.R. Polytechnic College	Head of the Department	02.06.1999	31.10.2007
5	K.S.Rangasamy Institute of Technology	Lecturer	25.06.1995	01.06.1999

09. Publication Details

	National Level*		International Level*	
	Journal(s)	Conference(s)	Journal(s)	Conference(s)
KSRCE	NIL	08	06	07
Others	NIL	NIL	NIL	NIL

Annexure*

10. Details of Proposal

Sl.NO	Title of the Proposal	Funding Agency Details	Date & Duration	Status
1	AC Power Production using Peltier	TNSCST	30.08.19 6 months	Applied

11. Details of Winter/Summer Schools Attended

Sl.NO	Name of the programme	Organized by	Duration
1	FDTP on “EC-6602- Antenna and Wave Propagation”	K.S.R. Institute for Engineering & Technology, Tiruchengode	27.11.2017 to 03.12.2017
2	A Course on PCB Design	K.S.R. College of Engineering associated with Raana Semiconductors Pvt.Ltd,Hosur	June-July 2016 (6 days)

3	FDTP on “Digital Principles & Design using Verilog HDL”	Velalar College of Engineering & Technology, Erode	16.06.2015 to 20.06.2015
4	Faculty Empowerment Workshop on “Electronic Circuits-II & Laboratory”	EQIP Foundation	02.12.2014 to 06.12.2014
5	Workshop on “High Impact Teaching Skills”	K.S.R. College of Engineering, Tiruchengode	31.05.2011 & 04.06.2011

12. Details of Workshops/ Seminars Attended

Sl. No	Name of the programme	Organized by	Durtion
1	Bio medical applications for dentistry, Implants & prosthesis of nanomaterials by using 3D printing techniques	Department of Health Research(DHR)- Indian Council of Medical Research(ICMR)	14.02.2019 & 15.02.2019
2	Accreditation Board of Engineering and Technology	K.S.R. College of Engineering, Tiruchengode	13.10.2017
3	Workshop on “Network Simulator-2”	K.S.R. College of Engineering, Tiruchengode	02.02.2017 to 04.02.2017
4	One Day Seminar on “Patent Filing in India and Other Countries”	K.S.R. College of Engineering, Tiruchengode	08.08.2016
5	Workshop on “Intellectual Property Rights”	K.S.Rangasamy College of Technology, Tiruchengode	06.08.2016
6	Safety Analysis of Nuclear Power Plant using Computational and Fuzzy Techniques	K.S.R. College of Engineering, Tiruchengode	03.05.2016 & 04.05.2016
7	Workshop on “Advanced Digital Logic System using CADENCE EDA Tools”	Karpagam Academy of Higher Education, Coimbatore	27.02.2016
8	Workshop on “VLSI Design Techniques using Mentor Graphics Tools”	M.P.Nachimuthu M.Jaganathan Engineering College, Chennimalai	18.02.2016
9	Workshop on “Journal Paper Writing and Preparation of Winner research proposal”	K.S.R. College of Engineering, Tiruchengode	19.08.2015 & 20.08.2015
10	Faculty Development Programme on “Embedded Systems with ARM-7 Microcontrollers”	Nandha Engineering College, Erode	27.05.2015 & 28.05.2015
11	Workshop on “Blooms Taxonomy and its Assessments”	K.S.R. College of Engineering, Tiruchengode	20.03.2015
12	Workshop on “Research Methodology”	Centre for Faculty Development Anna university, Chennai	10.12.2014
13	PLC and its Applications	Muthayammal Polytechnic College, Rasipuram	12.02.2007 to 14.02.2007
14	Accreditation-A Quality Education	S.S.M.College of Engineering, Komarapalayam	29.09.2006

13. Details of Workshops/Guest Lectures handled:

S.No	Name of the programme	Organized by	Duration
1.	Guest Lecture on “EC 6302-Digital Electronics”	Vidyaa Vikas College of Engineering and Technology,Tiruchengode	13.09.2017
2.	Guest Lecture on “EC 6302-Digital Electronics”	Vidyaa Vikas College of Engineering and Technology,Tiruchengode	21.09.2017

14.Details of Winter/Summer Schools/Workshops/Seminars organized:

Sl. No	Name of the programme	Sponsored by	Acted as	Duration	Coordinator
1	Workshop on “Basic Electronics”	A Division of CADD Centre Training Services Pvt.Ltd., Erode	Co-Coordinator	29.08.2015	Prof.P.S.Periasamy
2	IETE Sponsored Guest Lecture on “Recent Trends in Mobile Communication”	Self-Supported	Coordinator	03.09.2015	-

15. Details of the Subjects Handled (in KSRCE):

Sl. No	Academic Year	Semester	Name of the Subjects	Class	% of Pass
1	2019-2020	ODD	Digital Electronics Electronic Devices	II-ECE	On Going
2	2018-2019	EVEN	Electronic Circuits Satellite Communication	II – ECE A I-ME (CS)	54.35% 100%
		ODD	Signals and Systems Signals and Systems	II – ECE A II – ECE B	76.09% 84.09%
3	2017-2018	EVEN	Electronic Circuits Electronic Circuits Analysis and Design of Analog Integrated Circuits	II – ECE A II – ECE B I-ME (VLSI)	64.58% 92% 100%
		ODD	Digital Electronics Signals and Systems Advanced Digital System Design	II – ECE A II – ECE B I-ME (VLSI)	76.92% 86% 100%
4	2016-2017	EVEN	Electronic Circuits Satellite Communication	II – ECE A IV – ECE B	81.96% 100%
		ODD	Digital Electronics Satellite Communication	II – ECE A I-ME (CS)	73.77% 100%
5	2015-2016	EVEN	Electronic Circuits Satellite Communication	II – ECE B IV – ECE B	92.30 95.52
		ODD	Digital Electronics Satellite Communication	II – ECE A I-ME (CS)	76.05% 100%
6	2014-2015	EVEN	Electronic Circuits Analysis and Design of Analog Integrated Circuits	II-ECE C I-ME (AE) I-ME (VLSI)	94.64% 100% 100%
		ODD	Digital Electronics Advanced Digital System Design	II – ECE B I-ME (VLSI)	86.79% 100%
7	2013-2014	EVEN	Electronic Circuits Analysis and Design of Analog Integrated Circuits	II-ECE C I-ME (VLSI)	93.84% 100%

		ODD	Digital Electronics	II – ECE B	86.56%
			Advanced Digital System Design	I-ME (AE)	94.11%
				I-ME (VLSI)	100%
8	2012-2013	EVEN	Electronic Circuits-II	II – ECE B	81.81
			RISC Processor Architecture and Programming	I-ME (EST)	77.78%
		ODD	Digital Electronics	II – ECE B	57.35%
			Advanced Digital System Design	I-ME (VLSI)	100%
9	2011-2012	EVEN	Electric Circuits and Electron Devices	I –CSE B	35.2%
			Electronic Circuits-II	II-ECE A	95.58%
		ODD	Digital Electronics	II – ECE A	94.2%
			Advanced Digital System Design	I-ME (EST)	88.88%
			Advanced Digital System Design	I-ME (VLSI)	77.77%
10	2010-2011	EVEN	Electric Circuits and Electron Devices	I –CSE B	94.73%
			Electronic Circuits-II	II-ECE A	93.84%
		ODD	Digital Principles and System Design	II -CSE A & B	88.63%
11	2009-2010	EVEN	Electronic Circuits-II	II – ECE A & B	92.9%
		ODD	Digital Principles and System Design	II -CSE A & B	81.94%
12	2008-2009	EVEN	Electronic Circuits-II	II - ECE B	91.93%
		ODD	Digital Electronics	II – ECE A & B	95.31%

16. Additional Responsibilities:-

Sl. No	Details of Work	Work allotted by	Duration
1	Class Advisor	Head of the Department	June 2008 to May 2010
2	Class Incharge	Head of the Department	June 2010 to till date
3	Digital Lab Incharge	Head of the Department	June 2008 to till date
4	IETE Coordinator	Principal	June 2010 to June 2012
5	ISO Syllabus Incharge	Head of the Department	Nov 2008 to June 2012
6	AICTE Report Coordinator	Head of the Department	June 2011 to June 2018
7	Patent Incharge	Head of the Department	2015 to till date
8	NBA Coordinator	Head of the Department	June 2018 to till date
9.	FDP/Seminar Grant/ PSDC/ SDP/ Workshops Committee Incharge	Principal	June 2018 to till date

17.NPTEL Courses

S.No	Name of the Course	Duration	Status/Remarks
1.	Networks and Systems	12 weeks	Completed
2.	Microprocessors and Microcontrollers	12 weeks	Completed
3.	Digital Electronic Circuits	12 weeks	Completed
4.	Microelectronics:Devices to Circuits	12 weeks	On Going
5.	Switching Circuits and Logic Design	12 weeks	On Going
6.	Synthesis of Digital Systems	12 weeks	On Going

18. Self-Appraisal

- 3 Major Strengths:
1. Self confidence
 2. Hard worker
 3. Keen to learn new things

- 3 Major Weaknesses:
1. Tensed person
 2. Sensitive
 3. Less follow up

Signature

Annexure

LIST OF PUBLICATIONS

National Conference: 08

1. Ms.M.Mohana Priya,Mrs.P.Thilagavathi,"Embedded Based Sensing Carbon Monoxide from Hazards" in the national conference on Emerging trends in Electronics & Communication Engineering .
2. Mrs.P.Thilagavathi "Energy Efficient scheduling for cluster free wireless network with time bound data flow in the National Conference on Emerging Technology in Computer and Communication, Mach 15&16 2011.
3. Ms.E.Vidhya, N.Premalatha.A.Rasukutti, Mrs.P.Thilagavathi,"Embedded based Efficient Accident Alert System", in the National Conference on "Advancement in Electrical power Signal Processing and Communication Network Systems", March 7&8,2014.
4. Mrs.P.Thilagavathi,"Design and Implementation of Automated Blood Bank Database Using Arduino" in the National Conference on Convergent Communication Technologies and Telemedicine Networking, April 13,2016.
5. Mrs.P.Thilagavathi,"Train Compartment Communication System using RASBERRY PI2 Technology" in the National Conference on Frontiers in Communication and Signal Processing Systems, March 13&14 2017.
6. Mrs.P.Thilagavathi,"Smart Solider Assistance using Wireless Automatic Warmup Coat" in the National Conference on Innovation in Engineering and Technology,March 18 2017.
7. Mrs.P.Thilagavathi,"Advanced Train Compartment Communication using RASBERRY PI-2" in the National Conference on Emerging trends in Bioelectronics, Information and Communication Technologies, April 12 2017.
8. Mrs.P.Thilagavathi,"Land Survobot for Measuring Land Values and Soil Quality" in the National Conference on Emerging Trends in Communication and Information Technology,March 27,2018.

International Conference : 07

1. Ms.M.Meena, Mrs.P.Thilagavathi,"Automatic Docking System with Recharging and battery Replacement for Surveillance Robot" in proceedings of the international conference on "Emerging Trends in Computer & Communication Technologies", pp.183-184,2012.
2. Mr.Meenatchi Sundaram.S, Mrs.P.Thilagavathi, "Low Power Memory Design using signal Feed through Scheme Flipflop" International Conference on Intelligent Engineering Systems ,April 2014.
3. Ms.Gayathri,Mrs.P.Thilagavathi,"A Modern Hybrid Test Compression Technique for ATE", International Conference on Electrical, Electronics and Computer Engineering, 11th & 12th March, 2015

4. Mr.C.Arun Prasath, Mrs.P.Thilagavathi, Dr.A.M.Basha, "EM Algorithm Based Rain Pixel Recovery for Dynamic Videos", International Conference on "Advances in Computing, Control, Communication, Automation and Structural Engineering", 10th & 11th April 2015.
5. Mrs.P.Thilagavathi,G.Navinraj,"SOC Based Modern Hybrid Test Compression", International Conference on Electrical, Electronics and Computer Engineering, 22nd & 23rd March 2016.
6. Mrs.P.Thilagavathi,"Improving Fault Coverage and reduce the number of test data in test cube by using dynamic ISFR reseeding", International Conference on Recent Innovations in Engineering, Technology and Management, 22nd March 2019.
7. Mrs.P.Thilagavathi,"Hybrid Test Data Compression using CLCBCC and Run Length Encoding", International Conference on Advanced Science and Engineering Research, 23rd March 2019.

National Journal : Nil

International Journal : 06

1. Ms.M.Meena, Mrs.P.Thilagavathi,"Automatic Docking System with Recharging and battery Replacement for Surveillance Robot" in the International Journal of Electronics and Computer Science Engineering, ISSN-2277-1956,Volume 1, Number 3, pp-1148-1154.
2. Mr.Meenatchi Sundaram.S, Mrs.P.Thilagavathi, "Optimization of PDP for Memory Design using Signal Feed through Scheme" , International Journal of Software & Hardware Research in Engineering, ISSN 2347-4890 Volume 2 issue 3, (2014) pp-84-88.
3. Mr.C.Arun Prasath Mrs.P.Thilagavathi, Dr.A.M.Basha, "EM Algorithm Based Rain Pixel Recovery for Dynamic Videos", International Journal of Applied Engineering Research, ISSN Volume 10 Issue 38,(2015), pp- 28794-28797.
4. Mrs.P.Thilagavathi, Dr.A.Mahabub Basha, Mr.G.Navinraj,"SoC Based Modern Hybrid Test Compression", International Journal of Engineering Science & Management Research, ISSN 2349-6193.
5. Mrs.P.Thilagavathi, Mr.G.Navinraj,'Improvement of test Data Compression using Hybrid Technique"Journal of Scientific Research in Computer Science & Information Technology Volume 1 Issue 1 (2016)
6. Mr.D. Sathishkumar, Mrs.P.Thilagavathi," Improving fault coverage and reduce the number of test data in test cube by using dynamic LFSR reseeding" International Journal of Intellectual Advancements and Research in Engineering Computations Volume-7 Issue-1 (2019), ISSN:2348-2079,pp- 1530-1536.