K.S.R. COLLEGE OF ENGINEERING, TIRUCHENGODE – 637 215.

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

FACULTY PROFILE

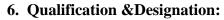
1. Name : B.Yuvarani

2. Designation : Assistant Professor

3. Date of Birth : 20.11.1979

4. Date of Joining : 14.06.2010

5. Professional Membership : ISTE



	At the time of Joining	Now	
Qualification	M.E	M.E	
Designation	Lecturer	Assistant Professor	

7. Specialization : Power Electronics and Drives

8. Area of Interest : Power Electronics

9. Experience & Promotion Details (Chronological):

SL.NO.	Name of the	Designation	Period	
	Organization		From	To
1.	K.S.R. College of Engineering	Assistant Professor	14.06.2010	Till Date



LIST OF PUBLICATIONS

National Conference: 02

- 1. B.Yuvarani presented a paper entitled "An Integrated Model of Clustering and routing in Wireless Sensor Networks" in the National Conference on Emerging Trends in Electrical Sciences at Institute of Road and Transport Technology, Erode held on 27th and 28th February 2013.
- 2. B.Yuvarani presented a paper entitled "Smart Bore well Optimizer & Monitoring System to Enhance Bore well Life" in the 6th National Conference on Emerging Trends in Engineering and Technology at NANDHA College of Technology, Erode held on 16th March 2019.

International Conference: 04

- 1. B.Yuvarani, Sasikala presented a paper entitled "Collision free permutation on-chip Network by using VHDL for SoC Applications" in the International Conference on Emerging Technology at Paavai Engineering College Technology, Namakkal held on 2nd March 2014.
- 2. B.Yuvarani, Sasikala, presented a paper entitled "VLSI Implementation of free Permutation network for Multiprocessor SoC Applications" in the International Conference on Innovations in Information, Embedded and Communication systems at Karpagam College of Engineering, Coimbatore held on 13th and 14th March 2014.
- 3. B.Yuvarani, S.Ramya, presented a paper entitled"Frequency domain equalization for delta modulation in mobile communication"in the international Conference on Electrical, Electronics and computer engineering at Vivekanandha Institute of Engineering and Technology for Women, Tiruchengode held on 2015
- 4. B.Yuvarani, presented a paper entitled" Reduction of leakage current from a pwm modulation for three-phase CH7 inverter in a transformerless photovoltaic systems"in the international Conference on Electrical, Electronics and computer engineering at Al-Ameen Engineering college, Erode held on 10th and 11th April 2018.

National Journal: Nil

International Journal: 05

- B.Yuvarani, C.B.Venkatramanan published a paper entitled "SHE-PWM Control for Cascaded voltage source Multilevel inverter based on GA Optimization" in the European Journal of Scientific Research , Print: ISSN 1450 –216X,Vol-69,No-3, pp 449-460(Nov 2012).
- 2. B.Yuvarani, Sasikala published a paper entitled "Collision free permutation on-chip Network by using VHDL for SoC Applications" in the International Journal of Emerging Technology in Computer Science and Electronics, (ISSN: -----), Special Issue 1, Vol.7 (March-2014).

- 3. B.Yuvarani, S.Ramya, presented a paper entitled"Frequency domain equalization for delta modulation in mobile communication" in the international Journal of imanager's journal on communication Engineering and systems,vol-3,No 4, August-October 2014
- 4. B.Yuvarani, P.Karthick,R.Muralidharan,M. Pavithra,P.Pavithra presented a paper entitled "Smart Borewell Optimizer And Monitoring System To Enhance Borewell Life" in the South Asian Journal of Engineering and Technology, vol ,s1, pp33-38, MARCH 2019
- 5. M.Sornalatha, K. Yamuna, A. Vasanthi & B. Yuvarani presented a paper "A Non-Invasive Way to determine Blood Type Based on Antigen Property" in the International Academy of Science Engineering and Technology, Vol-10,issue-2, pp 17-24, March 2021