

K.S.R. COLLEGE OF ENGINEERING

(An Autonomous Institution)
Tiruchengode, Tamil Nadu – 637 215.

Event Schedule

1	Name of the Event (Seminar/Workshop/Conference/FDP/Any other)	One Day Webinar
2	Name of the Organizer	Department of Electrical and Electronic Engineering
3	Date of Event	17.12.2021
4	Platform	Online/Google meet
5	Title of the Event	Latest Trends and Opportunities in VLSI
6	Name of the Co-ordinators	Dr.V.Ravi,Prof/EEE Dr.R.Sankarganesh,ASP/EEE
7	Total Participants	46
8	Objectives of the Event	To be successful completion of the event the participants have gained knowledge about Latest trends and opportunities in VLSI field


CO-ORDINATOR




PRINCIPAL

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K.S.R. KALVI NAGAR,
TIRUCHENGODE-637 215



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REPORT ON LATEST TRENDS AND OPPORTUNITIES IN VLSI

(17.12.2021)

K.S.R.College of Engineering, Department of Electrical and Electronics Engineering conducted one day webinar on Latest trends and opportunities in VLSI. This programme was presided over by Principal Dr.P.Senthilkumar.

One day webinar on Latest trends and opportunities in VLSI was organized by department of Electrical and Electronics Engineering hands on with **Er.SampathDharmalingam**, R & D Engineer, Broadcom Inc.,S1, Wipro Electronic City, Doddathogur, Begur Hobli Bangalore was the resource person of this programme.

The purpose of the one day webinar on latest trends and opportunities in VLSI was to discuss about the trend in design and manufacturing of very large scale integrated circuit shows an ongoing move towards smaller devices on increasing wafer dimensions. CMOS has become the prevailing technology due to its high speed and packing density coupled with low power consumption. New technologies have emerged to further increase circuit speed and to reduce design and technology constraints. The webinar ended with a vote of thanks to the guests and participants by Dr.R.Sankarganesh,ASP/EEE, K.S.R. College of Engineering.


COORDINATOR




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**DEPARTMENT OF
ELECTRICAL AND ELECTRONICS ENGINEERING**

**Webinar on
“LATEST TRENDS AND OPPORTUNITIES IN
VLSI”**

Date: 17.12.2021 | Time: 10.30 am

Resource Person

**Er. Sampath Dharmalingam
R & D Engineer,
Broadcom Inc.
S1, Wipro Electronic City,
Doddathogur, Begur Hobli,
Bangalore – 560 100.**



Convener

**Dr.S.Ramesh
Professor and Head / EEE, KSRCE**

Co-ordinators

**Dr.V.Ravi, Prof./EEE
Dr.R.Sankar Ganesh, ASP/EEE**




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TIRUCHENGODE-637 215**

K.S.R. COLLEGE OF ENGINEERING



(An Autonomous Institution, Affiliated to Anna University, Chennai &
Accredited by NAAC with 'A' Grade)

K.S.R. Kalvi Nagar, Tiruchengode – 637 215,
Namakkal DT, Tamil Nadu.

We cordially invite for a
Webinar On
“Latest Trends and Opportunities in VLSI”

Resource Person

Er. Sampath Dharmalingam

R & D Engineer,
Broadcom Inc.

S1, Wipro Electronic City
Doddathogur, Begur Hobli
Bangalore - 560100

*has magnanimously agreed to
deliver a lecture*



All are Welcome


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Google Meet Link: <https://meet.google.com/ato-bmes-neb>

Date: 17.12.2021 (Friday), 10.30 A.M.

AGENDA

Prayer Song

Welcome Address : Dr. V. Ravi

Professor / EEE,
K.S.R. College of Engineering.

**Presidential
Address**

: Principal

K.S.R. College of Engineering

**Introduction of
the Chief Guest**

: MoC (Student)

Chief Guest Talk : Er. Sampath Dharmalingam

R & D Engineer,
Broadcom Inc.,
S1, Wipro Electronic City,
Doddathogur, Begur Hobli,
Bangalore, 560100.

Vote of Thanks : Dr.R. Sankar Ganesh

Associate Professor / EEE,
K.S.R. College of Engineering.

National Anthem




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K.S.R. College of Engineering

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(Accredited by NAAC 'A' Grade)

K.S.R. Kalvi Nagar, Tiruchengode – 637 215, Namakkal DT, Tamil Nadu

Department of Electrical and Electronics Engineering

Organizes

One Day Webinar on

“LATEST TRENDS AND OPPORTUNITIES IN VLSI”

Date: 17.12.2021 | Time: 10.30 am

Resource Person

Er. Sampath Dharmalingam

R & D Engineer,

Broadcom Inc.

S1, Wipro Electronic City
Doddathogur, Begur Hobli
Bangalore, 560100



Convener

Dr.S.Ramesh

Professor and Head / EEE, KSRCE

Call Us



9942730303, 9842752364

Co-ordinators

Dr.V.Ravi, Prof./EEE

Dr.R.Sankar Ganesh, ASP/EEE

Google Meet Link : <https://meet.google.com/ato-bmes-neb>

E Certificate will be provided for the All the Participants



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K.S.R. COLLEGE OF ENGINEERING: TIRUCHENGODE – 637 215

(Autonomous)

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Webinar on

“Latest Trends and Opportunities in VLSI”

17th December, 2021

The figure consists of four screenshots from a Google Meet session titled "Webinar on VLSI Design".

- Top Left Screenshot:** Shows the presenter, Sampath Dharmalingam, in a large video window. The meeting controls at the bottom show the time as 11:44 AM.
- Top Right Screenshot:** Displays a grid of participants' video feeds. The "People" sidebar on the right lists participants including 517 MONISHWA..., 504 BHARATH..., and 504 BHARATHKUMAR E. The time is 11:52 AM.
- Bottom Left Screenshot:** Shows a slide titled "DESIGN FLOW" with a circular diagram illustrating the VLSI design process: Requirements Specification → Architecture Design → Implementation → Verification. The time is 11:52 AM.
- Bottom Right Screenshot:** Shows a slide titled "VLSI" with a list of topics: Very Large Scale Integrated Circuits, IC Design (ASIC, FPGA, Microcontroller, Processor), Processor - Moore's Law, Semiconductor Technology, IC Design (ASIC, FPGA, Microcontroller, Processor, SoC), Logic gates construction, and ASIC - Application Specific Integrated Circuits. The time is 11:24 AM.



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TIRUCHENGODE-637 215

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Webinar on

"LATEST TRENDS AND OPPORTUNITIES IN VLSI"

Date: 17.12.2021 (Friday)

Time: 10.30 AM

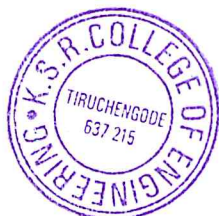
ATTENDANCE SHEET

S.No.	Reg. Number	Name of the Student	Year	Signature
1	1814537	S. VIVERA	IV / EEE	S. Vivera
2	1814506	S. ELAMATHI	IV / EEE	S. Elamathi
3	1814530	J. SHARMILA	IV / EEE	J. Sharmila
4	1814008	R. Jayapal	IV / EEE	R. Jayapal
5	1814522	K. NITHESHWARAN	IV, EEE	K. Nitheshwaran
6	1814005	DINESHRAJAN . R	IV - EEE	D. Rajan
7	1814535	Vignesh. . E	IV - EEE	V. Vignesh
8	1814518	M. VARAYANAN	IV - EEE	M. Varayan
9	1814009	Karananth. E	IV - EEE	K. Karan
10	1814001	Aakash .	IV - EEE	A. Aakash
11	1814536	VIGNESH. R	IV EEE	V. Vignesh
12	1814003	M. Deepanraj	IV EEE	M. Deepanraj
13	1814501	N. Ajith	IV - EEE	N. Ajith
14	1814503	ARUN RAJ	IV EEE.	A. Arun
15	1814516	M. MANIVEL	IV th EEE	M. Manivel
16	1814533	V. VOLLINGIRI	IV th EEE	V. Vellingiri
17	1814508	S. ELIYASU	IV th EEE	S. Eliyasu
18	1814007	M. Hari Krishnan	IV th EEE	M. Hari Krishnan
19	1814504	F. BHARATHKUMAR	IV - EEE	F. Bharathkumar
20	1814519	K. NOVEEN	IV - EEE	K. Noveen
21	1814512	S. JEEVANANTHAM.	IV - EEE	S. Jeevanantham
22	1814012	S. RAMKUMAR	IV - EEE	S. Ramkumar
23	1814529	V. SELVAKUMAR	IV - EEE	V. Selvakumar
24	1814531	E. SHIBINSOSWA	IV - EEE	E. Shibinsoswa
25	1814534	M. VEDARAJAN	IV - EEE	M. Vedarajan



26	1814520	M. NAVANEEL KUMAR	IV - EEE	M. Naveen
27	1814511	P. Jagadeeshwaran	IV - EEE	P. Jagadeesh
28	1814521	B. Nimal Nathan	IV - EEE	B. Nimal Nathan
29	1814528	Ks. Santhosh	IV - EEE	Ks. Santhosh
30	1814517	S. Monishwaran	IV - EEE	S. Monishwaran
31	1814516	GUNASEELAN. S	IV - EEE	G. Gunaseelan
32	1814507	Geengo. M	IV - EEE	Geengo. M
33	1814513	Jegomathy.	IV - EEE	J. Jegomathy
34	1814525	RANJITH. P	IV - EEE	P. Ranjith
35	1814509	GOKULRAJ. D	IV - EEE	D. Gokulraj
36	1814002	R.A. Aswinkumar	IV - EEE	R. A. Aswinkumar
37	1814014	S.K. Sathish	IV - EEE	S. K. Sathish
38	1814004	K.G. Dhinagan	IV - EEE	K. G. Dhinagan
39	1814502	M. Ajith Kumar	IV - EEE	M. Ajith Kumar
40	1714028	Hariprasad. K	IV - EEE	K. Hariprasad
41	1814011	manoranjith. S	IV - EEE	S. Manoranjith
42	1814016	M. Sridhar	IV - EEE	M. Sridhar
43	1814523	K. Periyannan	IV - EEE	K. Periyannan
44	1814017	M. Sriram	IV - EEE	M. Sriram
45	1814532	Thamarai Selvan. V	IV - EEE	V. Thamarai Selvan
46	1814524	G. prabhakaran	IV - EEE	G. Prabhakaran
47				
48				
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V. Prithvi
FACULTY IN CHARGE



[Signature]
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[Signature]
HOD/EEE