



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

INDUSTRY OFFERED COURSE 2025 - 2026

S. No.	Name of the Coordinator	Name of the Course	Resource Person Details	No. of participants	Duration
1.	Mr.K.Karuppanasamy Mrs.D.Gowthami	Advanced Embedded Systems Applications & IoT Programming	Dr. S. Mahavapandian Director – Embedded Systems	87	05.01.2026 to 12.01.2026
2.		IoT and Edge AI Implementation	Mr. L. Amutha Swami Nathan Embedded Senior Design Engineer Taras Systems and Solutions, Bangalore		18.01.2026 to 25.01.2026
3.	Dr.T.M.Sathishkumar Mr.R.Ravichandran Mrs.M.Dharani	RTL Design Using Verilog HDL	Mr. Venkatesh Rajakutti Director – VLSI Mr. J. Jes Evans Daniel VLSI Design Engineer	73	05.01.2026 to 12.01.2026
4.	Dr.R.Poornima Mr.M.Udhayakumar Mr.P.Sivasankar Rajamani	Functional Verification Using System Verilog and UVM	Mr. A. S. Arshad Basha VLSI Engineer Taras Systems and Solutions, Bangalore.		18.01.2026 to 25.01.2026