

**Department of Electrical and Electronics Engineering**

**Research Grant-in Aid Details**

**A. Research Project (Faculty)**

Sl. No	Name of the Coordinator (s) & Department	Name of the Organization / Agencies	Title of the Project	Sanctioned* Details (Letter No. & Date)	Amount Sanctioned (Rs)	Status
1.	Dr.M.Ramasamy Dr.M.Vijayakumar (Consultant)	The Deputyship for Research and Innovation, Ministry of Education, Saudi Arabia	Improved High Voltage Gain Multi-Port Switched Quasi Z-Source DC-DC Converter for the Integration of Renewable Energy Sources	IF2-PSAU/2022/01/2279 7. DT.01.11.2022	27,50000	Completed
2.	Dr. T.R. Sumithira	DST-WOS-A	Single Phase matrix converter topology in based Uninterruptable Power Supply	SR/WOS-A/ET-35/2009(G) Dt. 23.06.2011	13,44,000	Completed

## B. Student Project

Sl. No	Name of the Coordinator (s) & Department	Name of the Organization/ Agencies	Title of the Project	Sanctioned* Details (Letter No. & Date)	Amount sanctioned (Rs)	Status
1.	E.Kannan	TNSCST	Automatic Yeast Filling and Sealing Machine Using PLC	LT.NO.TNSCST/SPS /AR/20019-20, Dated : 08.03.2020	7,500	Completed

## C. MODROBS Grant

Sl.No	Name of the Coordinator (s) & Department	Name of the Organization/ Agencies	Title of the Project	Sanctioned* Details (Letter No. & Date)	Amount sanctioned (Rs)	Status
1	Dr.S.Ramesh	AICTE	Modernization of the Existing Measurements & Instrumentation Laboratory with an Emphasis on Real-time Computerized Measurement Systems	Ref.No.:12/ AICTE/RF ID/MOD(Policy-3)Pvt-57/2012-13 Dt. 13.03.2013	19,25000	Completed