

K.S.R. COLLEGE OF ENGINEERING, TIRUCHENGODE – 637 215.
(AUTONOMOUS)
DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING
FACULTY PROFILE

01. Name : S. Vadivel
02. Designation : Assistant Professor
03. Date of Birth : 10.07.1988
04. Date of Joining : 04.06.2012
05. Qualification :



Sl. No.	Name of the Degree	Specialization	Name of the Institution / University	Name of the University	Year of		Class & % of Marks
					Admitted	Completed	
1.	Ph.D	Information and Communication Engineering	Anna University, Chennai	Anna University, Chennai	2014	2022	-
2.	M.E	Applied Electronics	Bannari Amman Institute of Technology	Anna University, Chennai	2010	2012	First Class with Distinction & 9.51
3.	B.E	Electronics and Communication Engineering	Muthayammal Engineering College	Anna University, Chennai	2006	2010	First Class with Distinction & 81%

06. Experience, Promotion & Designation Details (Chronological):

a. Teaching:

Sl.No.	Name of the Institution	Designation	Nature of work	Period	
				From	To
1.	K.S.R. College of Engineering	Assistant Professor	Teaching	04.06.2012	Till Date
Total year of Experience(s)				11.10	

b. Industry:

Sl.No.	Name of the Organization	Designation	Nature of work	Period	
				From	To
-	-	-	-	-	-
-	-	-	-	-	-
Total year of Experience(s)				-	

07. Publication Details:**a. Conference Details:****National:**

Sl. No.	Title of the Research paper	Name of the author(s)	Publisher / Journal Name	Month & Year of publication	DOI & ISSN No.	IF / SJR / SNIP / H index / K index
2011-2012						
1.	Design of 64-Bit Low Power Parallel Prefix Adder in Different Word Length: Comparative Study	Vadivel S & Nehru K	National Conference on Signals, Systems & Security	February 2012.	-	-
2014- 2015						
1.	A systematic study of various radio propagation models for a hybrid MAC Protocol in sensor networks	Vadivel S & Kiruthika S	Sixth National conference on advanced telecommunication technologies	March 2015	-	-
2015-2016						
1.	Trailing Mobile Slinks to Data Coverage Protocol for Wireless Sensor Networks	Vadivel S	National Conference on Cutting Edge Technologies in Information and Communication	March 2016.	-	-
2017-2018						
1.	Security Verification For Outsourcing Image Data Using Multi Auditing Protocol	Vadivel S	National Conference on Recent Trends in Information, Communication and Computing Technologies	March 2018.	-	-

International:

Sl.No.	Title of the research paper	Name of the author(s)	Publisher / Journal Name	Month & Year of publication	DOI & ISSN No.	IF / SJR / SNIP / H index / K index
2011-2012						
1.	64-Bit Parallel Prefix Adder for High Speed Arithmetic Circuits	Vadivel S & Nehru K	First International Conference on Advances in Communication and Computing	January 2012	-	-

2.	Design of 64-Bit Parallel Prefix VLSI Adder for High Speed Arithmetic Circuits	Vadivel S & Nehru K	IEEE International Conference on Computing, Communication and Applications	February 2012	ISSN: 2325-6001, DOI: 10.1109/ICC CA.2012.617 9204, Print ISBN: 978-1-4673- 0270-8.	
3.	CLRCL Full Adder Based Low Power Multiplier Architectures	Vadivel S & Nehru K	International Conference on Information, Communication and Embedded Systems	February 2012	-	-
2014-2015						
1.	A Systematic Study of Various Radio Propagation Models for a Hybrid Mac Protocol in Sensor Networks	Vadivel S	Sixth International Conference on Advanced Telecommunication Technologies, K.S.R. College of Engineering,	March 2015	-	-
2.	Dynamic Reliable Low Energy Cost Routing Algorithm for Wireless Ad-Hoc Networks	Vadivel S & Vasanthi T	International Conference in Synergistic Evolutions in Engineering	March 2015	-	-
2016-2017						
1.	Low Power Two Stage Dynamic Comparator Circuit Design for Analog to Digital Converters	Vadivel S & N S Nithya	International Conference on Computer Communication and Informatics	Jan. 05 – 07, 2017	DOI: 10.1109/ICCCI.2018.8441497	-
2017-2018						
1.	A Pragmatic Algorithm Approach Based Secure And Reliable Web Application For Student Information Management	Vadivel S & Prakash M	International Conference on Advancements an Innovations in Engineering, Technology and Management,	December 2017	-	-
2019-2020						
1.	Light Weight Secure Data Sharing In Mobile Cloud Computing	Vadivel S Praveen Kumar G Savundharya S S Prabhakaran	AICTE Sponsored International Conference on Data Science and Big Data Analytics for sustainability	February 2020	-	-
2020-2021						
1.	Power Efficient Low Power Full adder based on Adiabatic Logic	Vadivel S N S Nithya	International Conference on Engineering and Advancement in Technology	July 2021	-	-

b. Journal Details:
National:

Sl. No.	Title of the research paper	Name of the author(s)	Publisher / Journal Name with full Expansion	Month & Year of publication	DOI & ISSN No.	IF / SJR / SNIP / H index / K index
-						

International:

Sl. No.	Title of the research paper	Name of the author(s)	Publisher / Journal Name with full Expansion	Month & Year of publication	DOI & ISSN No.	IF / SJR / SNIP / H index / K index	Impact Factor
2011-2012							
1	CLRCL Full Adder Based Low Power Multiplier Architectures	Vadivel S & Nehru K	International Journal of VLSI and Embedded Systems	January-April 2012	Vol 03, Issue 01	-	-
2013-2014							
2.	Incorporating Mobility to Sensor Networks During Node Failure and Their Analysis For Radio Propagation Models	Vadivel S & Kiruthika S	Journal of Communication Engineering & Systems	2014	Vol 4, No.1	-	-
3.	Latent Palm Print Matching Based on Minutiae Features for Forensic Applications	Vadivel S R.Abinaya, R.Abinaya, S.Vidhya,	International Journal of Communication and Computer Technologies,	March 2014	Vol 02. No.09 Issue: 01. ISSN No : 2278-9723	-	-
2014-2015							
4.	Dynamic Reliable Low Energy Cost Routing Algorithm for Wireless Ad-Hoc Networks	Vadivel S & Vasanthi P	International Journal for Scientific Research & Development	March 2015	Vol. No. 03, Issue No. 01, Pp. No. 1010 to 1012	-	-
5.	Secured and Smart Rationing for Public Distribution System Using RFID and GSM	Vadivel S & Kiruthika S	Journal of Microcontroller Engineering and Applications	March 2015	Vol 02. Issue No. 02, Pp. No. 21-28	-	-
6	A Systematic Study of Various Radio Propagation Models for a Hybrid Mac Protocol in Sensor Networks	S.Kiruthika, S.Vadivel	International Journal of communication and computer Technologies	April 2015	Volume 03 – Issue: 01 page 31 - 36		
2016-2017							
1.	Privacy Aware Data Aggregation Mechanism For Mobile Sensors	Vadivel S	Journal of Recent Research in Engineering and Technology	November 2017	Vol No. 04, Issue No. 02, Pp. 17 to 25	-	-

2.	A Pragmatic Algorithm Approach Based Secure And Reliable Web Application For Student Information Management	Vadivel S & Prakash M	International Journal of Innovations & Advancement in Computer Science	December 2017.	Vol. No.06, Issue No. 12, Pp. No. 179 to 186	-	-
2018-2019							
1	IRIS Image Based Diabetic Prediction Using Fuzzy Clustering Algorithm And SVM Classification Model	S Vadivel K.Kavin V.Manivannan M.Pradeep S.Pradeep	South Asian Journal of Engineering and Technology	April 2019	Vol 8 No S 1 (2019): Volume 8	-	-
2019-2020							
1	Light Weight Secure Data Sharing In Mobile Cloud Computing	Vadivel S Praveen Kumar G Savundharya S S Prabhakaran	International Journal of Research and Advanced Development (IJRAD), ISSN: 2581-4451	March 2020	Volume 4, Issue 1, pp. 40-45,	-	-
2021-2022							
1	Design of Adder based on Logical Gates for High Speed Low Power Robotic Applications	Vadivel S N S Nithya	Dynamic Systems and Applications	September 2021	30 (2021) No. 10, 1669 – 1682	-	-
2022-2023							
1	Image Enhancement by matrix based computation analysis using Gaussian Mixture Model	Vadivel S Bharath Kishore R Keerthana R Ganesan K	JXAT Journal	Feb 2023	Volume XV, Issue II	-	-
2	An Image Contrast Enhancement By Using Two-Dimensional Discrete Wavelet Transform	S. Vadivel, R. Praveenth, J. Samuvel, N. Siragirivelan	JXAT Journal	Feb 2023	Volume XV, Issue II	-	-

Summary:

	National Level		International Level	
	Journal(s)	Conference(s)	Journal(s)	Conference(s)
At KSRCE	-	3	12	4
At others colleges	-	1	1	3

8. Details of Monographs, Books & Patterns:

Sl.No.	Title	Name of the author(s)	Publisher Name	Year of publication
-	-	-	-	-

9. Details of Proposal:**Public Sector:**

Sl.No.	Title of the Proposal	Details of Funding Agency	Date & Duration	Status
1	Two Days Seminar on Safety Analysis of Nuclear Power Plant using Computational and Fuzzy Techniques	SERB,DST	4.05.2016 & 5.05.2016	Sanctioned

Private Sector:

Sl. No.	Title of the Proposal	Details of Funding Agency	Date & Duration	Status
-	-	-	-	-

10. Details of Winter/Summer Schools Attended (all faculty enrichment programme): (FDP)

S.No.	Name of the programme	Organized by	Date &Duration
1.	“FinFET, Nano-Sheet cell design , Now & Road Ahead” ; An educational perspective	Microwind, Pune	September 14 to 16, 2022
2.	Two Day Online workshop on VLSI Circuits Design and Device Modeling: Recent Trends and Advancements	Thiagarajar College Of Engineering Madurai-625 015	25th & 26th, November 2021
3.	Faculty Development Programme on Practical Cyber security from Scratch	SRM Institute of Science and Technology	13.07.2020 to 17.07.2020
4.	NPTEL Online Certification – Computer Architecture and Organisation	IIT-Kharagpur	Jan-Apr 2019 &12 Weeks
5.	NPTEL Online Certification – Microprocessors and Microcontrollers	IIT-Kharagpur	Jan-Apr 2018&12 Weeks
6.	NPTEL Online Certification – Introduction to Modern Application Development	IIT-Kanpur	Jul-Sep 2017 & 8 Weeks
7.	AICTE Sponsored Quality Improvement Programme Seven days Short Term Course on Analog Circuit Design	Coimbatore Institute of Technology, Coimbatore	22nd April to 28th April 2016`
8.	Faculty Development Programme on Microprocessors	Anna University, Chennai	21th May to 27th May 2015.

	and Microcontrollers		
9.	ISTE STTP on Introduction to Design Algorithms	IIT-Kharagpur	27th April to 30 th April 2015

11. Details of Workshops/ Seminars Attended:

Sl.No	Name of the programme	Organized by	Date &Duration
1.	Signal Processing using MATLAB	K.S.R College of Engineering	21.06.2020
2.	Webinar on Study of Switched Mode Power Supply	Mohamed Sathak A J College Of Engineering	18.06.2020
3.	Programming 8051 using C	Velalar College of Engineering and Technology	09.06.2020
4.	IoT based Smart Water Management System using Machine Learning Approach	SRM Institute of Science and technology	28.03.2020
5.	How to write research articles for scientific journals and Ph.D thesis on computational Engineering	SRM Institute of Science and technology	12.09.2019
6.	How To Write A Research Paper	K.S. Rangasamy College of Technology	28.07.2019
7.	Safety Analysis of Nuclear Power Plant using Computational and Fuzzy Techniques	K.S.R. College of Engineering	03.05.2016 – 04.05.2016 & 2 days
8.	NPTEL Workshop	K.S.R. College of Engineering	11.03.2017 & 1 day
9.	Infosys Campus Connect – UI Technologies	K.S.R. College of Engineering	29.03.2017 – 31.03.2017 & 3 days
10.	Effects on Environment and Human Health Due to E-Waste	Velalar College of Engineering and Technology	22.02.2018 – 23.02.2018 & 2 days
11.	Role of DST in promoting S&T ecosystem in India avenues and opportunities	K.S. Rangasamy College of Technology	18th November 2017

12. Details of Winter/Summer Schools/Workshops/Seminarsorganized:

Sl.No	Name of the programme	Sponsored by	Acted as	Duration	Convener
1.	International Workshop on Ethical Hacking & Cyber Security	ISTE	coordinator	Two days	Convener (Dr.A.Rajivkannan)

2.	Two Days Seminar on Safety Analysis of Nuclear Power Plant using Computational and Fuzzy Techniques	SERB	Convener	Two days	Convener (Dr.A.Rajivkannan)
3.	National level “Entrepreneurship Awareness Camp”	EDI-NSTEDB	coordinator	Three days	Convener (Dr.A.Rajivkannan)
	National Conference on Recent Trends in Information, Communication and Computing Technologies (NC-RTICCT’17)	K.S.R. College of Engineering Association With Tamilnadu College Events	Convener	One day	Convener (Dr.A.Rajivkannan)
4.	National level “Entrepreneurship Awareness Camp”	EDI-NSTEDB	Coordinator	Three Programme for Three days	Convener (Dr.A.Rajivkannan)
5.	National level “Entrepreneurship Awareness Camp”	EDI-NSTEDB	Coordinator	Two Programme for Three days	Convener (Dr.A.Rajivkannan)

13. Details of Guest Lecturer / Resource Person / Session Chair inside and outside the college: (FDP / Training / Workshop / Seminar / Conference)

Sl. No	Name of the programme	Organized by	Date &Duration
1.	9th Annual Polytechnic Faculty Convention of ISTE Tamil Nadu Section	K.S.R. Polytechnic College	14.10.2022

14. Area of Interest :

Research (Ph.D)	Degree (PG)	Degree (UG)	Experience
Low Power VLSI Design	Low Power VLSI Design	Microcontrollers	VLSI Design and Microprocessors, Microcontrollers

15. Professional Membership: ISTE

16. Details of the Subjects Handled (in KSRCE):

S. No	Academic Year	Semesters	Name of the Subject	Class	% of Pass
1	2012-2013	ODD	Analog and Digital Communication	II CSE A	75 %
				II CSE B	85 %

		EVEN	Microprocessors and Microcontrollers	II CSE B	87 %
2	2013-2014	ODD	Electric Circuits and Electron Devices	II CSE A	80 %
				II CSE C	83 %
		EVEN	Microprocessors and Microcontrollers	II CSE C	89 %
3	2014-2015	ODD	Electric Circuits and Electron Devices	II CSE C	80 %
			Digital Signal Processing	IV CSE B	75 %
		EVEN	Microprocessors and Microcontrollers	II CSE A	91 %
4	2015-2016	ODD	Electric Circuits and Electron Devices	II CSE A	78 %
				II CSE B	79 %
		EVEN	Microprocessors and Microcontrollers	II CSE A	89 %
5	2016-2017	ODD	Electric Circuits and Electron Devices	II CSE A	79 %
				II CSE C	81 %
		EVEN	Mobile Application Development	III CSE	100 %
6	2017-2018	ODD	Professional Ethics	III CSE	100 %
		EVEN	Mobile Application Development	III CSE	100 %
7	2018-2019	ODD	Computer Architecture	II CSE A	86 %
		EVEN	Ethical Hacking	IV CSE	97 %
8	2019-2020	ODD	Logical Design of Digital Computer	II CSE A	75%
9	2019-2020	EVEN	Internet of things	IV CSE	100%
10	2020-2021	ODD	Professional Ethics	IV CSE A B	90%
11	2020-2021	EVEN	Microprocessors and Microcontrollers	II CSE A	100%
12	2021-2022	ODD	High Speed Networks	IV CSE	100%
13	2021-2022	EVEN	Microprocessors and Microcontrollers	II CSE	86%
14	2022-2023	ODD	Entrepreneurship Development	III CSE	100%
15	2022-2023	EVEN	Microprocessors and Microcontrollers	II CSE	

17. Additional Responsibilities:

a. College Level

S.No.	Details of Work	Work allotted by	Duration
1.	Associate Placement Officer	Principal	June 2021 to Till Date
2.	Deputy Placement Officer	Principal	June 2019 to September 2021
3.	VIP Hospitality	Principal	Jan 2020

b. Department Level

S.No.	Details of Work	Work allotted by	Duration
1.	Placement Coordinator	HOD	Nov 2016 to September 2021
2.	Faculty Profile In charge	HOD	June 2012 to July 2013
3.	Industrial Visit In charge	HOD	June 2015 to July 2017
4.	Research and Development Coordinator	HOD	June 2016 to July 2017
5.	Academic coordinator	HOD	June 2015 to July 2017
6.	Class Advisor	HOD	October 2021 to Till date

18. Self-Appraisal

Major Strengths

1. Time Management
2. Positive Attitude

Major Weaknesses:

1. Impatient
2. Straight Forwardness

Signature with Name
(Vadivel S)